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A case study on hydrogen generation from solar energy with MPPT control strategy

Thiago F. Rech^{✉1,*}, Tailan Orlando^{✉1}, André L. Kirsten^{✉1}, Roberto F. Coelho^{✉1}

¹Federal University of Santa Catarina, Power Electronics Institute, Florianópolis – SC, Brazil.

e-mail: thiagofrech@gmail.com^{*}; tailan_orlando@hotmail.com; kirsten.andre@inep.ufsc.br; roberto.coelho@ufsc.br.

^{*}Corresponding author.

ABSTRACT The current mainstream method of global hydrogen production relies on steam reforming of natural gases, which leads to CO₂ emissions. On the contrary, renewable-powered water electrolysis offers a sustainable alternative for hydrogen production. The growing adoption of hydrogen as an energy storage solution holds potential for new applications, including ancillary electric grid and transportation services. Those high-power applications require a high output current (900 A) and relatively low output voltages (160-220 V). To address these requirements, a system is designed from a photovoltaic array, two step-down interleaved converters, and an intermediary DC bus to power a 10-kW electrolyzer. The first stage allows the implementation of the maximum power point tracking of the photovoltaic array, ensuring optimal energy conversion, while the second stage supplies the electrolyzer with constant current. A droop-based strategy is proposed to regulate a floating bus voltage within a specified range. Controller Hardware-in-the-Loop results show the feasibility of the proposed control strategy.

KEYWORDS Electrolyzer, Green Hydrogen, Interleaved Step-Down Converter, Maximum Power Point Tracking, Power Electronics.

I. INTRODUCTION

This paper presents an complete study case of off-grid hydrogen generation from a photovoltaic system. The hydrogen gas is currently used in refineries for ammonia synthesis, or converted into energy carriers, such as methanol, methane and liquid hydrocarbons [1]. Hydrogen can also be used directly by burning it in the same way as oil and gas [2], such as in rocket fuels or converted to electricity. However, the mainstream method of global hydrogen production is based on steam reforming of natural gas, which results in CO₂ emissions. A promising sustainable alternative to produce hydrogen is from renewable-power water electrolysis [3].

To be considered green hydrogen, the gas has to be produced from green energy sources and green processes: for example, water electrolysis from renewable energy [4]. To date, the presence of renewable energy in the grid is increasing fast. In Brazil, in 2023, more than 20% of the electric energy comes from photovoltaic (PV) and wind [5]. It has been shown that the high penetration of this renewable energy sources in the energy mix is expected to be a greater challenge due to their intermittency and uncontrollability [6].

Among the future applications of hydrogen, a hydrogen-based storage system could be used as an energy vector to convert water (2H₂O) to hydrogen (2H₂) and oxygen (O₂) during a surplus, therefore the gas may be used directly in a given application or used to generate electricity during an energy deficit [1]. The adoption of hydrogen as an energy vector has been said to facilitate new applications, including

ancillary services for the electric grid, transportation, and more [7], [8]. In this scenario, a large quantity of green hydrogen must be produced.

Due to the need for high direct currents, traditional industrial water electrolyzers are usually connected to the grid, relying on thyristors and diodes for rectification [9], [10]. The switching at line frequency with thyristor-based rectifiers creates high amplitude harmonics in the supply current and voltage, which results in a challenge to comply with grid code [10].

To overcome this drawback, some papers have been proposing the direct connection of renewable sources and electrolyzers, resulting in off-grid hydrogen production systems [3], [11]. Due to the intermittent nature of renewable energy production, it is essential to evaluate the compatibility between the energy source and the electrolyzer. Studies have shown that small Proton Exchange Membrane (PEM) electrolyzer systems (up to 60 kW) are capable of responding to 100% ramp-up and ramp-down events within sub-second intervals during variable renewable energy load-following tests [12]. To ensure reliable performance, a well-designed and fully integrated system is required, with particular attention to the power electronics stage [11].

Considering the case in which the input source is a photovoltaic array, the factors that influence hydrogen production are diverse and can be analyzed from different perspectives [10]. In the context of flexibility, hydrogen generation technology can be characterized by the following aspects [13]:

- 1) The rate of change in energy consumption;
- 2) The lower operating limit or minimum opening level;
- 3) The time for startup and shutdown.

The safe rate of change for hydrogen production by PEM technology ranges from 10%/s up to 50% [13]. This makes it well-suited to accommodate the intra-daily variations in incident irradiance typically observed in PV systems, which occur at a much slower rate of approximately $\pm 0.1\%/s$ [13]. However, sudden changes in atmospheric clarity can induce significantly faster irradiance fluctuations. The combined system of the power electronics driver and the electrolyzer stack must respond rapidly to track these variations in solar energy. Together, they ensure that the hydrogen production process remains stable and efficient under dynamic operating conditions.

The minimum operating rate of energy consumption characterizes the minimum power that must be supplied by the source to the electrolyzer. This is relevant for sizing the converter's active and passive components as well as defining control strategies [12].

The startup time of an electrolyzer, also called *cold start*, includes the initialization time of the control unit and the removal of air from the piping. For the shutdown of the electrolyzers, similar procedures are applied. In summary, the above-mentioned times and rates mainly depend on the electrolyzer technology and constructive factors of the *stack*. Lower-power systems have smaller thermal dynamics, leading to shorter times and consequently greater flexibility [14]. These limitations influence the control and operation strategy of the electrolyzer [12], [15], [16]. For off-grid generation systems, the following strategies are mainly found:

- Strategy 01: the electrolyzer operates at a variable setpoint throughout the day according to production [13], [17]–[20].
- Strategy 02: the electrolyzer is operated at a fixed power level, which is below its nameplate capacity. A battery energy storage system (BESS) provides or stores energy as necessary to maintain a constant operating point for the electrolyzer [13], [17].
- Strategy 03: a BESS supports the electrolyzer only during periods in which the photovoltaic power is insufficient to meet the electrolyzer's minimum operating limits [13], [17], [20].

The strategies are more clearly displayed by Fig. 1, where the irradiance changes throughout the day. Fig. 1 a) where the electrolyzer is turned on at the start of the day and off at the lower operating limit. In Fig. 1 b), the extra photo-generated power is stored in a short-term battery, and utilized during the night. In Fig. 1 c), a small battery is only used to support the minimal operational point of the electrolyzer.

Most economic and power electronics studies disregard the effects of sudden changes in atmospheric clarity and temperature, which is not ideal when aiming to integrate a complete solution for H_2 production. Therefore, analyzing a more

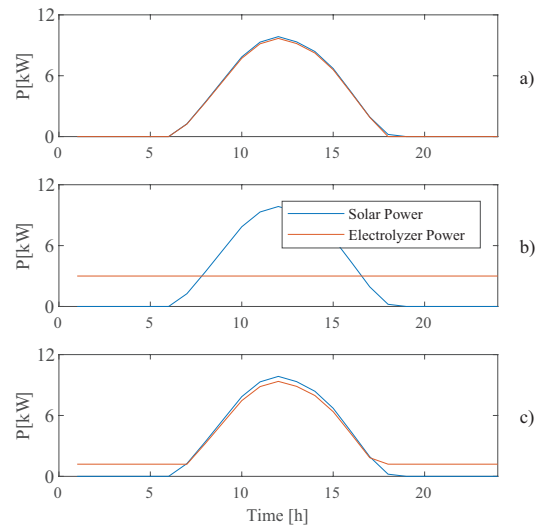


FIGURE 1. Solar powered hydrogen production strategies: a) Strategy 01; b) Strategy 02; and c) Strategy 03.

realistic scenario is important. To illustrate this difference, irradiance and ambient temperature data from INPE [21], measured at the Florianópolis substation on a minute-by-minute basis, were used. Fig. 2 a) shows the measured irradiance for the 1st, 2nd, and 3rd day of April 2019. Fig. 2 b) presents the estimated junction temperature, calculated based on the irradiance and ambient temperature data. The output power of a system (12 kW peak), shown in Fig. 2 c), is derived from the solar irradiance and the estimated junction temperatures.

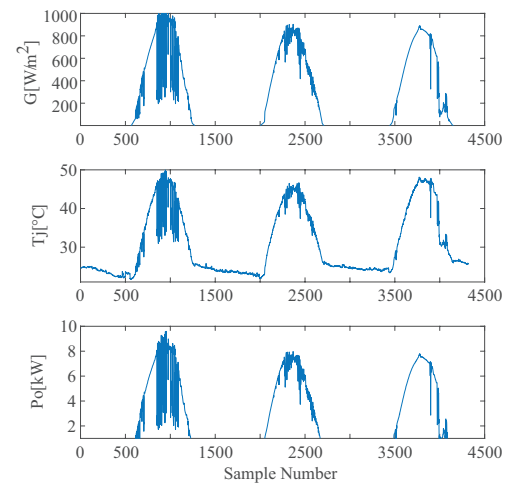


FIGURE 2. More realistic generation scenario, a) irradiance, b) estimated junction temperature and c) estimated available output power for April 1st, 2nd and 3rd of 2019 in Florianópolis.

As can be seen in the realistic curves, the derivative of output power is much higher due to shadowing and changes in the atmospheric clarity. A solar-powered hydrogen production system must be able to withstand the intermittency of the power source.

In this scenario, the introduction of newer power electronics components could offer advantages, which would require modular or interleaved converters to distribute the supplied currents across appropriate levels for semiconductors, such as insulated-gate bipolar transistors (IGBTs) and Metal-Oxide-Semiconductor Field-Effect Transistor (MOSFETs).

In principle, many DC/DC converters can be used as an interface between the PV array and the electrolyzer. The main requirements of these converters are:

- Ability to transmit low current ripple to the electrolyzer: primary studies have shown that a high current ripple reduces the efficiency of alkaline and PEM electrolyzers [7], [22], [23] and has the potential to reduce the life span of PEM electrolyzers. [24], [25]. This phenomenon is still a key research question for water electrolyzers.
- High Electrical efficiency in the entire operational range of the electrolyzer: the converters must maintain high efficiency under varying load conditions, as the operating point of the electrolyzer can fluctuate throughout the day. High efficiency minimizes energy losses, reduces heat generation, and contributes to the overall economic viability of the hydrogen production system.
- High conversion ratio: depending on the difference between output and input voltages, the conversion ratio can reach high values, which, in a single non-isolated conversion scenario, could lead to prohibitive duty cycles.
- Reliability in case of component failure: since costs associated with system failure are high, therefore, functionalities as redundant switching devices, fault-tolerant designs, and protection mechanisms are mandatory.

Other relevant factors that should be accounted during the converter design for an electrolyzer include: output voltage range and reduced cost. Existing transformer-less DC/DC converters have been reviewed in [3] presenting multiple topologies. Among the possible transform-less topologies, various versions of the buck converter have also been proposed, such as the stacked buck converter [26]. High efficiency is a main requirement and with this aim, it has been proposed soft-switching converters, as for example the solution based on coupled inductors presented in [27]. Also, high-efficient converters based on the partial power processing concept [28], were quite largely proposed for PV applications, such as in [29].

One trend identified in [3] are the use of interleaved converters. They are a well-known approach to handle higher currents to offer better dynamic response and smaller overall filters. It involves connecting a few converters in parallel and controlling them with a phase-shift carrier. Combining the input and output currents of these phases and applying this technique effectively reduces both ripple and harmonics [24]. There are still multiple topological options, operational

voltages, and the ideal compromise between the number of interleaved phases and ripple mitigation to study [3].

For this application, the Interleaved Buck Converter (IBC) was selected [27]. The number of phases and the design of each component were described in [30]. For a proper evaluation of the proposed system, Hardware-in-the-Loop (HIL) is employed, and the feasibility of the proposed power stage and control strategy is shown. In contrast to conventional grid-connected topologies, the proposed architecture targets an off-grid configuration. The inclusion of a droop-based voltage regulation strategy enables autonomous operation under variable irradiance without communication between stages.

This paper is organized as follows. Section I presents the introduction, including the motivation and the main challenges associated with green hydrogen production from solar energy. Section II describes the overall system architecture, including the photovoltaic array, electrolyzer, and the interleaved converters. Section III outlines the proposed control strategy, encompassing the MPPT and droop-based control approaches, and provides a linearized state-space model to support the controller design. Section IV presents Hardware-in-the-Loop (HIL) simulation results, validating the effectiveness of the proposed system and control methods under realistic conditions. Finally, Section V offers a discussion of the results, highlighting the main findings, contributions, and potential directions for future research.

II. SYSTEM DESCRIPTION

The present work is part of a larger modular strategy to feed an electrolyzer with high-frequency power semiconductors (IGBT, MOSFETs) by paralleling multiple interleaved converters and distributing supplied power [31]–[33].

The final system will be used to study PEM electrolyzer operation, and a current reference will be provided from the central controller to each module. This work focuses solely on a single interleaved module's control strategy and design, which includes a two-stage power converter to drive a 10-kW equivalent PEM electrolyzer. The designed system is formed by a photovoltaic array, two step-down interleaved power converters, and an intermediary DC-bus to properly feed the electrolyzer, as shown in Fig.3.

The main electrical specifications of the system are shown in Fig. 3 and are displayed in Table 1. This data is considered for the proper converter design of both power stages.

A. Photovoltaic Array

The one-diode model is a widely used electrical representation of a photovoltaic (PV) description, represented by the circuit in Fig. 4. In this circuit, i_{ph} represents the photo-generated current, R_s is the series resistance accounting for electrical contact losses, and R_p is the parallel resistance modeling leakage current losses. The diode D models the semiconductor properties of the PV cells. The variables v_{pv} and i_{pv} are the output voltage and current, respectively. By

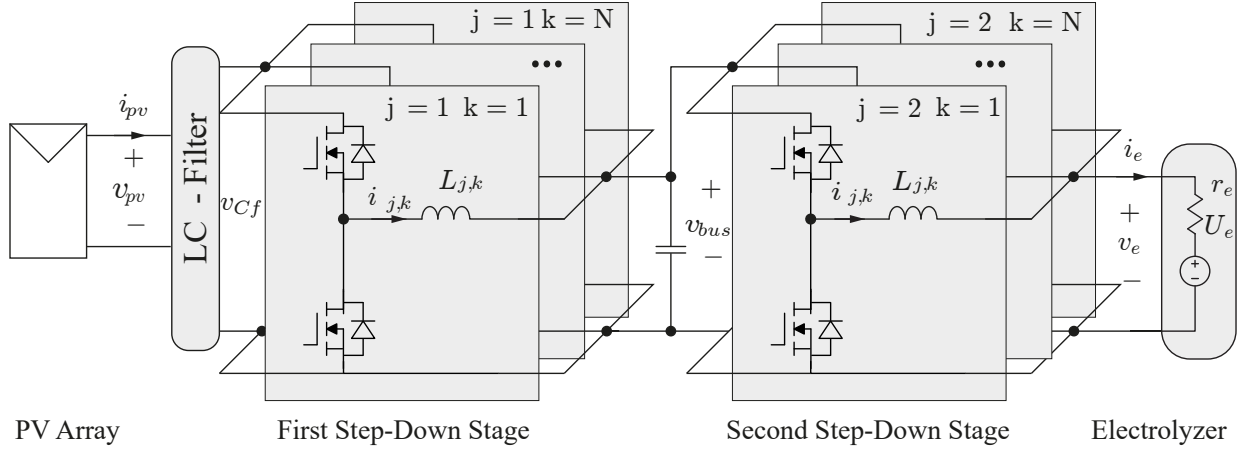


FIGURE 3. Proposed System Diagram.

TABLE 1. System parameters

System	Values
PV Array rated power	12 kW
V_{oc} @STC (PV open-circuit voltage)	493.5 V
V_{mp} @STC (PV maximum power point voltage)	400 V
Electrolyzer Voltage Range (v_e)	160 V - 220 V
Electrolyzer Activation Voltage (U_e)	130 V
Electrolyzer Internal Resistance (r_e)	0.03 Ω
Bus Voltage Range (v_{bus})	250 V - 350 V
Bus Capacitance (C_{bus})	200 mF
Input Filter Inductance of the First Stage ($L_{k,j}$)	2 mH
Input Filter Resistance of the Second Stage ($L_{k,j}$)	2 mH
Output Current (I_{nom})	62.5 A
Switching Frequency (f_s)	10 kHz

evaluating the one-diode model, it is possible to obtain a system of non-linear equations, which the solution result in the five unknown parameters of the model [34]. From this

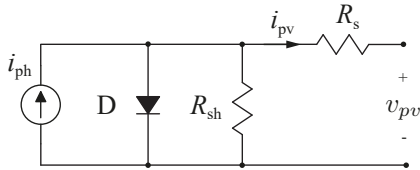


FIGURE 4. One-diode model of photovoltaic arrays.

procedure, it was possible to obtain the five parameters, of a single module Kyocera KC200GT photovoltaic module are obtained from the datasheet and the solution for the model parameters from [34]. The photovoltaic array consists of 4 strings connected in parallel, each string consists of 15 PV modules connected in series. According to [34], the one-diode model of a module can be extended to represent the behavior of the complete array. Therefore, adjustments must be made to the one-diode model parameters according to the number of modules connected in series and in parallel. The

adjusted parameters then represent the one-diode model of the complete array. The final electrical specification of the PV module and of the PV array under the Standard Test Conditions (STC) are detailed in Table I, while the respective I-V and P-V curves are shown in Fig. 5. It is evident that for lower voltages, the PV array behaves much similarly to a current source.

TABLE 2. System parameters

Parameter	Symbol	Single Module	Full Array
Output Power	P_{mp}^{STC}	200 W	12 kW
Voltage @MPP	V_{mp}^{STC}	26.3 V	394.5 V
Current @MPP	I_{mp}^{STC}	7.61 A	30.44 A
Voltage @OC	V_{oc}^{STC}	32.9 V	493.5 V
Current @SC	I_{sc}^{STC}	8.21 A	32.84 A
Ideality Factor	A	1.34	1.34

B. Input Filter

A LC filter was added between the PV array and the first stage to mitigate the circulation of high-frequency current through the PV array. The long cable required for the application carry an inductance, which is estimated and incorporated in the design. Finally, the filter cut-off frequency was tuned to be one decade below the switching frequency.

C. PEM Electrolyzer

The water electrolysis operation is an electrolytic process that decomposes water H_2O molecule into oxygen O_2 and hydrogen H_2 . Many different PEM electrolyzer models have been proposed in the literature, such as chemical models, static and dynamic electric models [13], [35].

Modeling hydrogen production is a complex task due to its dependency on a multitude of factors such as temperature, water flow, pressure and demand. Since the internal dynamics of the electrolyzer are significantly slower than those of

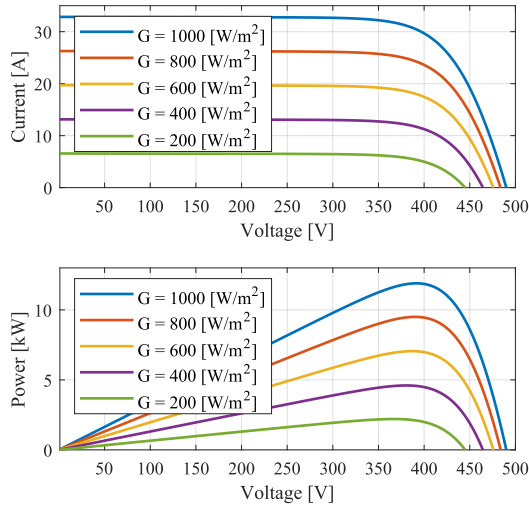


FIGURE 5. I-V and P-V curves of the photovoltaic array.

the converter, the systems are modeled separately. From the driver's perspective, fewer chemical and physics concepts are relevant. For instance, it is relevant to know the required current on the electrolyzer for a given production rate [36]. In alkaline and PEM electrolyzers, the molar flow ($\dot{H}_2$) depends on the number of cells (n_c) on the current applied (I_e), the Faraday efficiency (n_f), and on the Faraday constant (F), in accordance with:

$$\dot{H}_2 = n_f \frac{n_c I_e}{2F}. \quad (1)$$

For PEM-type electrolyzers, the Faraday efficiency is typically greater than 99% [36]. This means that the generated flow depends mostly on the current level. Current state-of-the-art PEM electrolyzers experience a degradation rate of 2-4% per year, resulting in an increase in operational voltage during their life cycle. As the electrolyzer ages, its internal resistance increases, resulting in a higher operational voltage, as presented in Fig. 6.

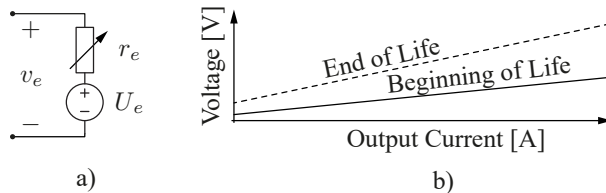


FIGURE 6. Effect of aging in PEM electrolyzer stack: a) simplified steady-state PEM electrolyzer model and b) V-I curve for old and new electrolyzers.

Therefore, the steady-state voltage-current characteristic of an electrolyzer [37] can be modeled as a temperature-dependent voltage source with an additional non-linear resistor in series. This resistor represents the internal losses of the electrolyzer. A simple $V \times I$ model for a given electrolyzer

is given by:

$$v_e(T) = U_e + i_e r_e \quad (2)$$

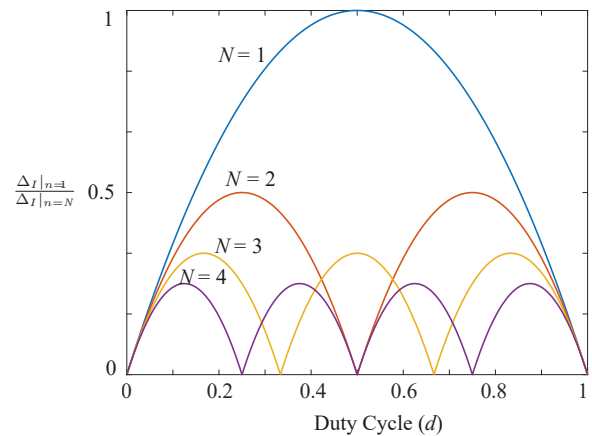
where U_e is the activation voltage, r_e is the internal resistance, which accounts for ohmic and electrochemical losses and v_e is the stack voltage.

D. Interleaved Buck Converter

Due to the high currents demanded by electrolyzers, several authors [18], [30], [38] have proposed parallelism and interleaving of multiple buck converters as a proper solution to drive them. The number of interleaving cells and/or parallel converters depends on multiple. In this work, 3 cells ($N = 3$) were found to be a good compromise between complexity and total losses. For a non-coupled Input Parallel Output Parallel (IPOP) Multicell (MC) converter formed by N interleaved cells, the relative ripple on the output is given by:

$$\Delta I = \left[\frac{v_{Cf} D}{f_s L_f} \left(1 - \frac{\text{floor}(DN)}{DN} \right) \right] [1 + \text{floor}(DN) - DN] \quad (3)$$

The inductance of a single cell (L_f) depends on the desired output current ripple (ΔI), the number of cells (N), instantaneous duty-cycle (D), and the switching frequency. ($f_s = \frac{1}{T_s}$). The function floor rounds down the product between the instantaneous duty cycle and the number of cells (DN). Fig. 7 presents the current ripple behavior across different numbers of switching cells. Since the first stage is not directly connected to the electrolyzer, a higher current ripple is acceptable. This work considers the inductance of both stages to be equal for simplicity, and that the output differential-mode-filter design is based on the current ripple established on the electrolyzer. Hence, to minimize the output current ripple, a duty cycle of 0.5 is optimal when $N = 2$; 0.333 and 0.666 when $N = 3$.


 FIGURE 7. Normalized output current ripple of an interleaved converter as a function of the number N of phases.

E. Bus Capacitor Dimensioning

The selection of the bus capacitor directly influences the system's dynamic response to power fluctuations and reference

changes. A well-dimensioned capacitor mitigates transient effects, preventing excessive voltage oscillations that could affect the electrolyzer's performance. Moreover, the capacitance value must be chosen to balance the trade-off between system stability, cost and volume.

By considering the proposed droop control order presented in Section III, and electrolyzer constraints, the proposed capacitance formulation ensures that the system operates within safe limits. The electrolyzer is still susceptible to reference changes above its maximum rate limit. The derived expressions provide a quantitative approach for selecting a feasible bus capacitor, ensuring that the power reference changes remain within the electrolyzer's operational constraints. The ramping rate of the electrolyzer indicates how fast it can react upon a change in power (from 10%/s of the current operating set point to +50%/s, depending on the manufacturer) [13]. In general, the slowest variables such as pressure, gas venting, and temperature, along with the control system, impose limits on the ramp rate, rather than the electrical capability of the stack:

$$\frac{\delta}{\delta t} P_e \leq (10\% - 50\%) P_n, \quad (4)$$

where P_e is the output power delivered and P_n is the nominal power rating of the electrolyzer. A few reasonable assumptions are made: (i) ideal reference tracking, such that the current reference equals the electrolyzer current, i.e., $i_{\text{ref}} = i_e$; and (ii) the energy stored in the inductors is negligible compared to that stored in the DC bus capacitor, i.e., $\frac{1}{2} C_{\text{bus}} V_{\text{bus}}^2 \gg N \cdot \frac{1}{2} L \left(\frac{I_L}{N} \right)^2$. Under these assumptions, the system can be represented as shown in Fig. 8.

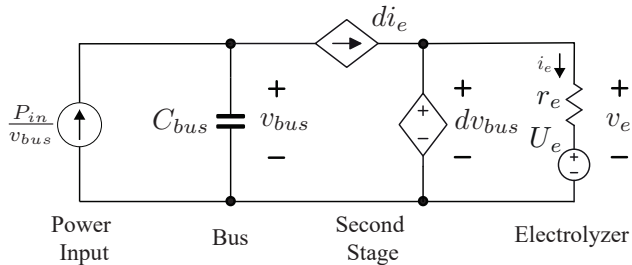


FIGURE 8. Block diagram of a power system with a DC bus and electrolyzer. The diagram illustrates the power input (P_{in}) and voltage (v_{bus}) to the bus, followed by the second stage and the electrolyzer, with respective voltage and current variables labeled as v_{bus} , i_e , and U_e .

Considering a sudden change in the input power, The capacitor voltage depends on the initial voltage $v_{bus}(0^-)$ and on the current drained by the second stage. The following system of equations is found for the second stage:

$$i_e = I_{nom} + m_i(v_{bus} - V_{nom}), \quad (5)$$

$$v_e = r_e i_e + U_e, \quad (6)$$

$$v_{bus} = v_{bus}(0^-) - \frac{1}{C_{bus}} \int_0^t i_e \frac{v_e}{v_{bus}} - \frac{P_{in}}{v_{bus}} dt. \quad (7)$$

By inserting (5) and (6) into (7), the following equation is found:

$$v_{bus} \frac{dv_{bus}}{dt} = - \frac{1}{C_{bus}} [r_e(I_{nom} + m_i(v_{bus} - V_{nom}))^2 + U_e(I_{nom} + m_i(v_{bus} - V_{nom})) - P_{min}]. \quad (8)$$

The same equation is extended to be represented in the canonical form of a polynomial and is rewritten in terms of three constants A_0 , A_1 , A_2 :

$$v_{bus} \frac{dv_{bus}}{dt} = A_0 + A_1 v_{bus} + A_2 v_{bus}^2, \quad (9)$$

where the constants A_0 , A_1 , A_2 , are defined as:

$$A_2 C_{bus} = -m_i^2 r_e, \quad (10)$$

$$A_1 C_{bus} = 2V_{nom} m_i^2 r_e - 2I_{nom} m_i r_e - U_e m_i, \quad (11)$$

$$A_0 C_{bus} = P_{min} - I_{nom}^2 r_e - U_e I_{nom} + EV_{nom} m_i - I_{nom}^2 m_i^2 r_e + 2I_{nom} V_{nom} m_i r_e. \quad (12)$$

This is a first-order nonlinear ordinary differential equation (ODE) [39], where all coefficients A_0 , A_1 , and A_2 are nonzero. The roots of the quadratic polynomial $A_2 v^2 + A_1 v + A_0 = 0$ determine the equilibrium points of the system:

$$V_0 = \frac{-A_1 \pm \sqrt{A_1^2 - 4A_1 A_2}}{2A_2}. \quad (13)$$

However, a complete closed-form solution for this expression is not available [40], and the solution must be obtained numerically. To enable analytical treatment, and determine the smallest feasible capacitance, the equation has to be linearized around a specific operating point V , resulting in a first-order linear separable differential equation:

$$\frac{d}{dt} v_{bus}(t) = \left(\frac{A_1 + 2A_2 V_0}{V_0 C_{bus}} \right) v_{bus}(t), \quad (14)$$

where V_0 is the equilibrium point for v_{bus} . This is an exponential decay equation, of which the symbolic solution is:

$$v_{bus}(t) = V_0 + (v_{bus}(0^-) - V_0) e^{\frac{A_1 + 2A_2 V_0}{V_0 C_{bus}} t}. \quad (15)$$

The electrolyzer current (i_e) and electrolyzer voltage (v_e) are found by substituting (5) in (6). The output power and its derivative are found. Since the highest rate of change the output power P_o occurs at the moment which v_{bus} is higher, i.e. ($t = 0$), to respect the rate change of the electrolyzer:

$$\frac{\delta}{\delta t} P_e = \frac{K m_i (U_e + r(I_{nom} + m_i(K - V_{nom}))(A_1 + 2A_2 V_0))}{C_{bus} V_0} + \frac{K m_i r (A_1 + 2A_2 V_0)(I_{nom} + m_i(K - V_{nom}))}{C_{bus} V_0}. \quad (16)$$

Therefore, to mitigate abrupt variations in output power, the bus capacitance must be sized above a threshold:

$$C_{bus} \geq \frac{Km_i(U_e + r(I_{nom} + m_i(K - V_{nom}))(A_1 + 2A_2V_0))}{\frac{\delta}{\delta t}P_eV_0} + \frac{Km_ir(A_1 + 2A_2V_0)(I_{nom} + m_i(K - V_{nom}))}{\frac{\delta}{\delta t}P_eV_0} \quad (17)$$

To evaluate the effect of the adopted approximations, a comparative analysis was carried out between the numerical solution and the algebraic approximation of (9), as well as the numerical solution derived from the original differential formulation presented in (14). Fig. 9 illustrates the outcome of this comparison. In this example, a 2% constraint on the rate of power variation was imposed, corresponding to a power ramp limit of 200 W/s. This constraint would lead to the selection of a relatively high capacitance value to support a 1 kW step reduction.

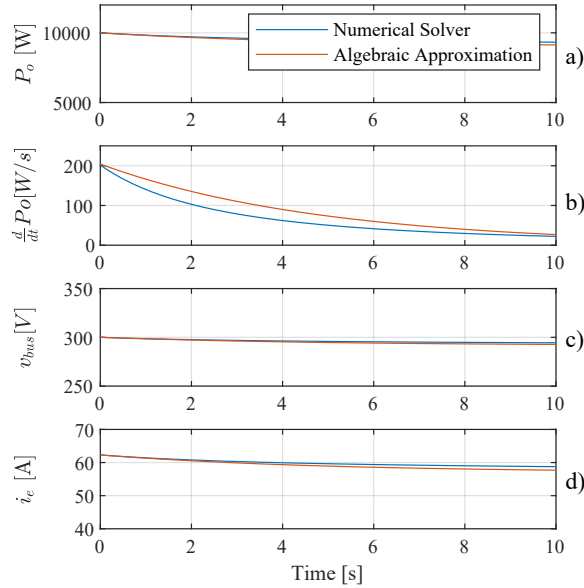


FIGURE 9. Comparison between the numerical solution of 9. In a), the output power, b) rate of change, c) bus voltage d) the electrolyzer current.

A practical capacitor sizing can be achieved by considering the specific electrolyzer, as illustrated in Fig. 10. The x-axis represents the acceptable rate of change in the electrolyzer power, while the right y-axis indicates the corresponding required capacitance. For instance, to limit the power variation to 20% of the nominal value, a capacitance of approximately 200 mF is required.

This capacitance requirement has direct implications on both system cost and physical integration. High-capacitance components suitable for high-voltage DC applications tend to be bulky and costly, often presenting additional challenges in terms of reliability, lifespan, and thermal management. Electrolytic capacitors, while offering a lower cost per unit capacitance, typically suffer from reduced operational life and higher equivalent series resistance (ESR). Therefore, the capacitor selection process must carefully balance the

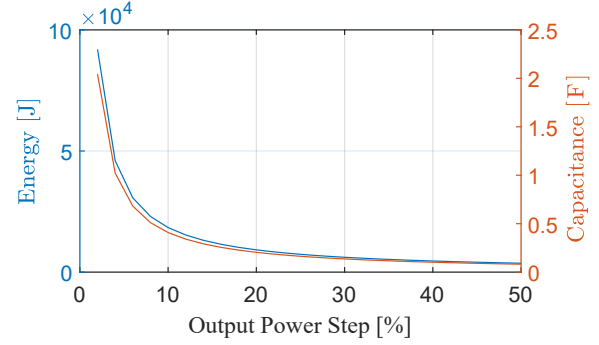


FIGURE 10. Required bus capacitance for different rates of power variation in the electrolyzer input.

dynamic performance requirements of the electrolyzer with the economic and volumetric constraints of the system.

III. CONTROL STRATEGY AND CONTROL ORIENTED MODEL

The system model described in Fig. 3 is divided into two stages: the first stage is composed of the photovoltaic array, the LC filter, the first buck interleaved; the second stage consists of the second interleaved buck converter and the electrolyzer. Unfortunately, the operation of multi-cell systems with common-duty-ratio cannot rely on natural balancing, therefore each cell was individually controlled [32].

A. Droop and MPPT Control Strategy

The system's control diagram is presented in Fig. 11. The two stages of the cascaded interleaved buck converter are controlled separately. The reference signal of each stage is dependent on the operational state. In the first stage, a current controller must be used to guarantee an equal current distribution among phases 1 to N . A voltage controller regulates the duty cycle to follow the voltage reference that is either a V_{mp} or a droop reference, depending on the operational state. For the second stage, the current controller either tracks the external current reference (I_N) or the droop response reference, in cases where environmental conditions cannot provide the required current.

The two main operational states are illustrated by Fig. 12. Depending on the power balance created between the PV array and the power consumed by the electrolyzer:

- In the first operational state ($v_{bus} < V_{nom}$) the PV array is maintained at maximum power point (MPP), and the current reference follows (18) until the bus voltage reaches the nominal operating point.
- In the second operational state ($v_{bus} > V_{nom}$), a voltage droop is applied, following (19), whereas the current is maintained at I_{nom} . The multiloop control strategy, presented in Fig. 12, ensures an equal current distribution among the phases and adequate reference tracking.

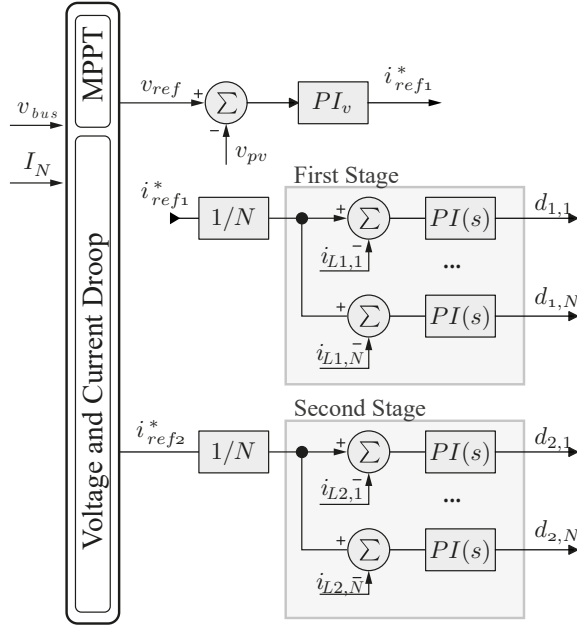


FIGURE 11. Diagram of the controller for both stages. For the first stage, the voltage reference (v_{ref}) is provided either from the MPPT or from the voltage droop, the output of the voltage controller is the current reference ($i_{ref,1}$). For the second stage, the current reference ($i_{ref,2}$) is either (I_N) or is given by the current droop.

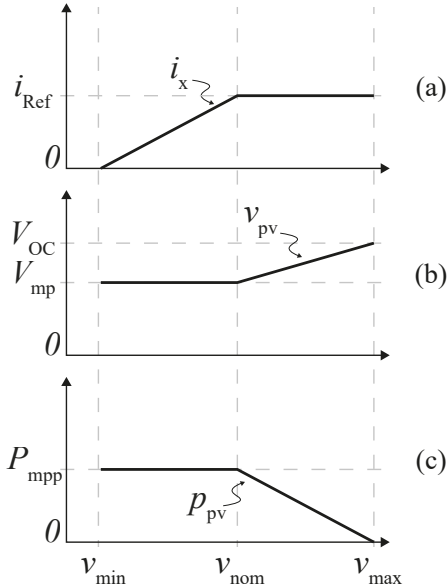


FIGURE 12. Droop strategy. (a) Current reference, (b) Voltage reference, (c) PV array power.

The current and the voltage reference for the second stage are given by

$$I_{ref,2} = I_{nom} + m_i (v_{bus} - V_{nom}), \quad (18)$$

$$v_{ref} = v_{mp} + m_v v_{bus} - m_v V_{nom}, \quad (19)$$

where the respective droop coefficients (m_i) and (m_v) are:

$$m_i = (I_{nom}) / (V_{nom} - V_{min}) = 0.5682, \quad (20)$$

$$m_v = (v_{mp} - v_{oc}) / (V_{nom} - V_{max}) = 2.0200. \quad (21)$$

B. MPPT algorithm

In the context of MPPT algorithms, methods such as neural networks, pattern search, fuzzy logic and genetic algorithms require data for training, which raises problems related to memory, time complexity and source of training data [41]. Indirect methods are also available in the literature and are supported by mathematical expressions [34]. A temperature-based MPPT strategy was chosen because it allows the control system to continuously use the MPP information to calculate the droop reference, even when not operating on the MPP, as presented in Fig. 13.

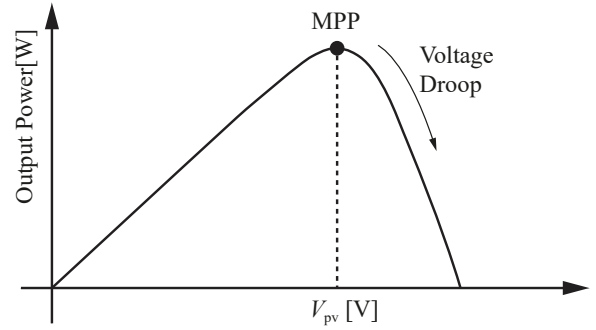


FIGURE 13. MPPT basic operation and voltage droop control [15].

The temperature-based MPPT calculates the MPP voltage reference to be used as an input to the controller:

$$v_{ref}(T) = v_{mp}(T) = V_{mp}^{STC} [1 + \beta_{mp} (T - T^{STC})], \quad (22)$$

where $\beta_{mp} = \gamma - \alpha$ is the thermal coefficient of V_{mp} , while α , β , and γ are the thermal coefficients related to I_{sc} , V_{oc} and P_{mp} , respectively given by the PV module datasheet. Since the temperature (T) may not always be available for direct measurement, it may be estimated (T_{es}) from the PV open-circuit voltage, obtained by opening the switches of the first stage, as in the fractional open circuit voltage method [15]:

$$T_{es} = \left(\frac{v_{oc}}{V_{oc}^{STC}} - 1 \right) \beta + T^{STC}. \quad (23)$$

To read the open-loop voltage, the switches of the first stage $S_{1,1:N}$ are opened for a small time interval, the current on the first stage (i_L) inductors flows through the diodes until it becomes zero. The total time to the input capacitor reaches the open-circuit voltage depends on the dynamic of the input filter and the operating point. In addition, a few meters of wire between the PV array and the first-stage converter has enough inductance to compose the filter inductor, however, the effect of the parasitic resistance that causes voltage drops, needs to be predicted. Therefore, the voltage v_{pv} must be reevaluated to its nominal input. Thus:

$$v_{pv} = v_{cf} + i_L r_C. \quad (24)$$

The dc-resistance r_C is estimated based on the wire length and its diameter. The average input current is estimated by

the sum of the inductor currents and the instantaneous duty cycle:

$$i_{ph} = d_{1,1}i_{L1,1} + d_{1,2}i_{L1,2} + d_{1,3}i_{L1,3}. \quad (25)$$

The control system and the MPPT are validated against the EN 50530 standard. This standard provides irradiancies profiles over a few minutes. The maximum theoretical, expressed in (26) power of the photovoltaic array is determined from the temperature (T) and irradiance (G) and then compared to the obtained output power. The input parameters G and T are updated every 250 ms, according to EN 50530 standard [42]. This standard was chosen because it defines a precise testing procedure for measuring the overall efficiency of grid-connected photovoltaic (PV) inverters. The standard specifies methods for measuring both static and dynamic MPPT efficiency, which evaluates the speed and precision of tracking under changing conditions to give a real assessment of the inverter's performance. Typhoon HIL Control Center records i_{ph} and v_{pv} at 100 Samples per Second (SPS) during the standard test (1900 seconds).

$$P_{mp}(T) = \frac{G}{G_{STC}} I_{mp}^{STC} V_{mp}^{STC} [1 + (\alpha_{mp} + \beta)(T - T^{STC})]. \quad (26)$$

The obtained curves are presented in Fig. 14 a cross-correlation higher than 0.99 is found, indicating statistical equivalence between the maximum available power (P_{MAX}) and the obtained power (P_{OB}). Also, an MPPT efficiency of 99.25% was obtained from the same data by comparing the total energy from the irradiance and temperature profile for the PV array against the obtained energy by employing the proposed MMPT strategy.

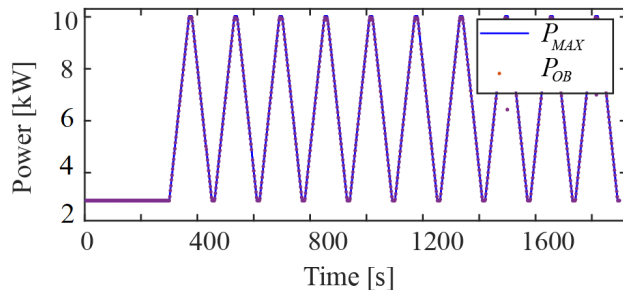


FIGURE 14. EN 50530 30%-100% along the y-axis: it is displayed maximum available theoretical power (P_{MAX} [W]) and the obtained output power simulated in Typhoon HIL Control Center (P_{OB} [W]).

C. Control-Oriented Linear Model

For the control design of the PI controller, both stages of the converter and input filter were modeled separately in state space for a generic N value of interleaved cells, in the form $\dot{x} = Ax + Bu$ and $Y = Cx + Eu$. The definition of the matrices C and E is based on the dynamic behavior being under study at the given time. Additionally, matrix E is null due to the absence of a direct output-input relationship. The first stage includes the PV Array, the LC filter, the first IBC.

The bus voltage is considered to be constant and at a nominal value. This stage requires reference tracking either for the MPPT voltage, or the droop voltage of the solar array, the output is used as a reference to the current on the inductors. The state vector x is formed by the current on the inductors ($i_{L1,1} \dots i_{L1,N}$) the voltage on the PV array (v_{Cpv}), the current on the filter inductor, and the voltage on the filter capacitor (i_{Lf} and v_{Cf}):

$$x_1 = [i_{L1,1} \dots i_{L1,N} v_{Cpv} i_{Lf} v_{Cf}]. \quad (27)$$

The vector of derivatives is the state of the current in the filter capacitor. The derivatives are defined by:

$$\begin{aligned} \frac{di_{L1,1}}{dt} &= \frac{1}{L_f} \{ (dv_{Cf} - v_{bus}) - r_L i_L \}, \\ &\dots, \\ \frac{di_{L1,N}}{dt} &= \frac{1}{L_f} \{ (dv_{Cf} - v_{bus}) - r_L i_L \}, \\ \frac{di_{Lf}}{dt} &= \frac{1}{L_f} (v_{Cpv} - r_f i_{Lf} - v_{Cf}), \\ \frac{dv_{Cf}}{dt} &= \frac{1}{C_f} \left(i_{Lf} - d \sum_{n=1}^N i_{L1,n} \right), \\ \frac{dv_{Cpv}}{dt} &= \frac{1}{C_{pv}} (i_{ph} - i_{Lf}). \end{aligned} \quad (28)$$

To complete the system model, the input vector u is defined by the duty cycle (d), the input current (i_{ph}), and the bus voltage (v_{bus}). So,

$$u_1 = [d \ i_{ph} \ v_{bus}]. \quad (29)$$

The matrices A , B , C , and E of the linearized system are obtained from the Jacobian matrix and vector of derivatives,

$$A = \left. \frac{\partial x}{\partial x} \right|_X, \quad B = \left. \frac{\partial x}{\partial u} \right|_X, \quad E = \left. \frac{\partial y}{\partial x} \right|_X, \quad C = \left. \frac{\partial y}{\partial x} \right|_X, \quad (30)$$

where X is the equilibrium point, determined by solving the system of equations presented in (28) when equal to zero. Therefore, the linearized state space system can be found. The open-loop transfer function of the inductors currents to d is given by:

$$\frac{G_{iL}}{d}(s) = C_1(sI - A)^{-1}B_1, \quad (31)$$

where C_1 is a empty matrix of which only $C_{1,1} = 1$, and the PV array voltage given by:

$$\frac{G_{v_x}}{G_{iL}}(s) = \frac{C_6(sI - A)^{-1}B_1}{C_1(sI - A)^{-1}B_1}, \quad (32)$$

where C_6 is a empty matrix of which only the element $C_{6,1} = 1$.

The second stage includes the bus capacitor, the second IBC, and the electrolyzer. This stage requires reference tracking of I_N or the current-droop on current on the inductors. The state vector x_2 is:

$$x_2 = [i_{L2,1} \dots i_{L2,N}], \quad (33)$$

and the derivatives $\dot{x}_2$ are:

$$\begin{aligned} \frac{di_{L2,1}}{dt} &= \frac{1}{L_{2,1}} dv_{bus} - r_s i_L - U_e - r_e \sum_{n=1}^N i_{L2,n}, \\ &\dots, \\ \frac{di_{L2,N}}{dt} &= \frac{1}{L_{2,N}} dv_{bus} - r_s i_L - U_e - r_e \sum_{n=1}^N i_{L2,n}. \end{aligned} \quad (34)$$

The open-loop transfer function of the inductor current to d is given by:

$$\frac{G_{i_l}(s)}{d(s)} = C_6(sI - A)^{-1}B_1, \quad (35)$$

where C_1 is an empty matrix of which only the element $C_{1,1} = 1$.

The PI current and voltage controllers, presented in Section III are tuned based on the frequency response method, within a 60° phase margin and a crossover frequency of $f_s/5$ (i.e 2 kHz) and 20 Hz, respectively, resulting in a proportional gain $k_p = 0.041$ and integral gain $k_i = 41.92$.

IV. HIL RESULTS

A switched model simulation of the proposed converter was implemented in Hardware-in-the-Loop (HIL). The PV system model and the converters switched models are implemented in Typhoon HIL 404, as presented in the diagram in Fig. 15.

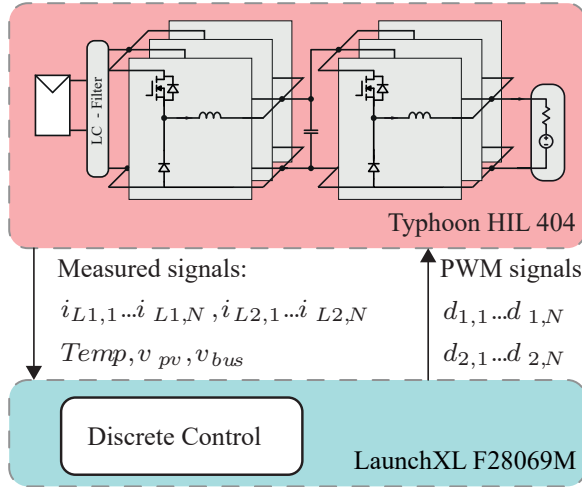


FIGURE 15. Diagram displaying the system.

The actual setup is presented in Fig. 16. A time execution of $100 \mu s$ was used in Typhoon HIL 404 that generated analog signals between 0 and 3.3 V, which the DSP reads. The discrete control was realized by a microcontroller TMS320F28069M from Texas Instruments. This device allows reading up to 16 Analog Signals and generates the ePWM signals for each of the 6 switching cells.

A SCADA application, shown in Fig. 17, was created in Typhoon HIL SCADA to monitor and control important variables such as irradiance and PV array temperature. Fig.17 displays the real-time SCADA, monitoring the PV curves, the PV voltage and other electrical variables of the system.

The control system sets the current provided to the electrolyzer to I_{nom} . The bus capacitor increases to almost 310 V. As can be seen, the bus voltage in Fig.18 b) increases until reaching steady state value. The voltage on the filter capacitor rapidly increases to V_{oc} , which is then recorded by

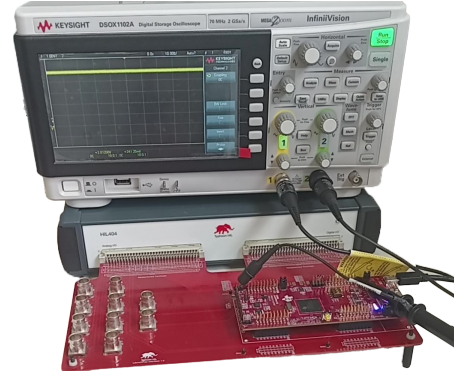


FIGURE 16. HIL device - Typhoon HIL 404 connected to a DSPF28069, a oscilloscope DSOX1102A displays the bus voltage.

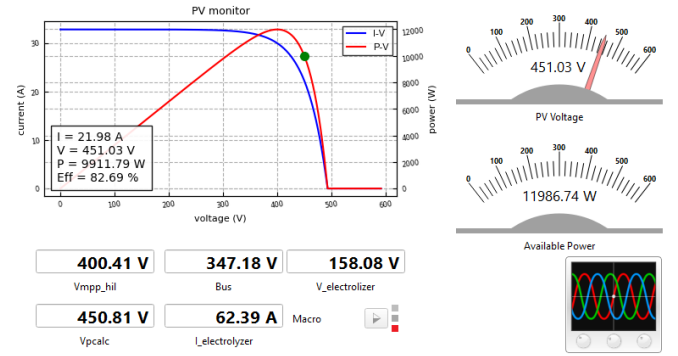


FIGURE 17. Real-time SCADA, monitoring PV curves and electrical variables of the system. In this scenario, the PV array is generating more power than the required from the electrolyzer.

the DSP. As can be observed, the bus voltage (in Fig. 18 b)) increases until reaching its expected steady-state operation. Fig. 18 a) shows the inductor currents ($i_{L2(1,2,3)}$), where a high-frequency ripple is observed due to the switching operation. The waveform demonstrates the expected behavior of the three-phase interleaved structure, with currents sharing the total load. Fig. 18 b) displays the voltages across the converter.

Fig. 19 presents the converter's main electrical quantities to validate the control system against the EN50530 standard [42]. The irradiance and temperature varies during the day. During the MPPT calculation, the measurement of V_{oc} is realized to estimate temperature and the MPP, for that, the first stage converter's lower and upper IGBT switches are opened so the current is brought to zero. The voltage on the filter capacitor quickly increases its voltage to V_{oc} , which is recorded by the DSP.

To calculate the efficiencies of the proposed two-stage power conversion system across its operational input power range, the power-losses tool of the Typhoon HIL platform was used. This analysis provides an insight into the efficiency distribution within the cascaded architecture. The IGBT IKW30N65H5 was considered, and other losses (gate-driver, inductors and capacitors) were considered equal to 20 W in

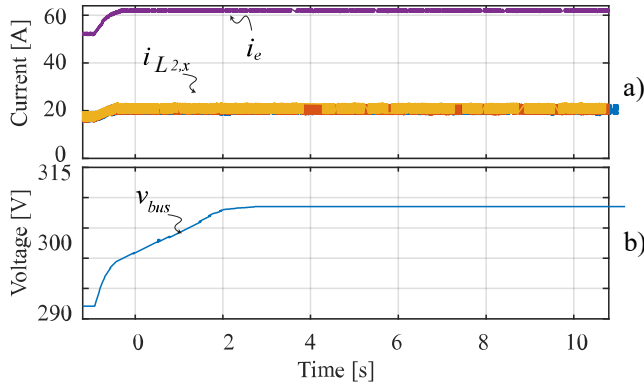


FIGURE 18. Main electrical quantities of the system operating with temperature-based MPPT: a) second stage inductor current ($i_{L1,2}$) and the electrolyzer current (i_e) and b) dc bus voltage (v_{bus}).

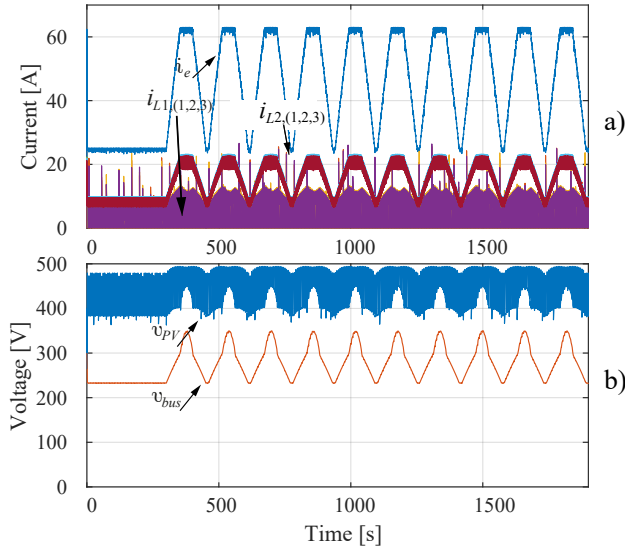


FIGURE 19. Main electrical parameters obtained from HIL SCADA controlled by the microcontroller TMS320F280969M during the validation against the EN50530 standard; a) first and second stage inductor currents; b) voltage on the photovoltaic array and voltage on the dc-bus; c) filter capacitor voltage and filter inductor current.

each stage. The result is illustrated in Fig. 20 illustrates four distinct efficiency metrics. First Stage: This curve represents the efficiency of the initial DC-DC conversion stage, η_1 . Second Stage: This curve represents the efficiency of the subsequent conversion stage, η_2 . This stage is responsible for the final output voltage regulation and ripple suppression, and its efficiency is calculated as the ratio of the converter's output power to the power received from the first stage, $P_{in,2}$. Converter Stage: This curve displays the overall DC-DC converter efficiency, η_3 , calculated as the product of the individual stage efficiencies ($\eta_3 = \eta_1 \cdot \eta_2$). This metric reflects the total electrical conversion performance of the cascaded topology. System: This curve presents the estimated overall system efficiency, η_t . This metric integrates the

electrical conversion efficiency with the MPPT performance, calculated as $\eta_t = \eta_3 \cdot 0.9925$. The 99.25% factor represents the steady-state tracking efficiency of the MPPT algorithm, providing a realistic approximation of the net power transfer from the photovoltaic array to the load under continuous operation.

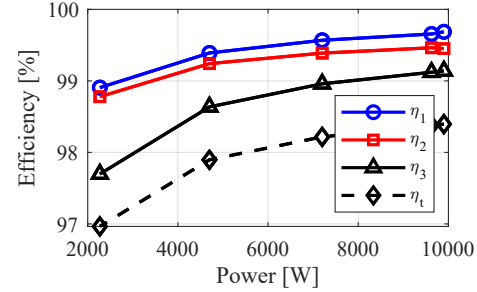


FIGURE 20. Estimate of the efficiency of the System using Typhoon HIL Losses Calculation.

V. CONCLUSION

This work has proposed the modeling, design, and control of an interleaved buck converter with N cells based on a linearized state space model for an electrolyzer system. The interleaved design allows for a current distribution among phases, which can be understood as its main advantage vsain the proposed application, since the voltage fluctuation on the input diminishes the potential advantage of the ripple cancellation of the interleaved converter.

The two-stage converter, along with the control strategy, allows the PV array to operate either MPP or on the required reference current of the electrolyzer. The MPPT was evaluated against the theoretical maximum power, according to EN 50530, and resulted in an efficiency of 99.25%. MPPT tracking and current reference tracking. These results confirm the proper operation of the control system, ensuring stable voltage and current waveforms throughout the MPPT process. The transient response observed in the waveforms aligns with the expected system dynamics during variations in irradiance and temperature. These results confirm the proper operation of the control system, ensuring stable voltage and current waveforms. However, the series connection of the stages reduces the global efficiency of the converter, a parallel strategy may be useful to reduce the power variation in the electrolyzer while maintaining efficiency levels.

A further limitation concerning the system design and the fluctuating voltage on the DC bus is the inability to fully exploit the ripple reduction capability intrinsic to the interleaved converter across the complete operational spectrum. Specifically for the three-phase configuration selected, this advantageous characteristic is partially lost when the duty cycle (D) is near 0.5 ($D \approx 0.5$). In this case, the ripple reduction is one-third in comparison with buck converter with the same output filter.

The proposed capacitor dimensioning, along with the droop control, results in a safe driver for the electrolyzer,

the strategy may result in conservative values for the bus capacitance. The final decision takes into account the limits on equipment, increase in cost and volume.

AUTHOR'S CONTRIBUTIONS

T.F.RECH: Data Curation, Formal Analysis, Investigation, Methodology, Software, Validation, Writing – Original Draft, Writing – Review & Editing. **T.ORLANDO:** Conceptualization, Formal Analysis, Investigation, Methodology, Software, Validation, Visualization, Writing – Review & Editing. **A.L.KIRSTEN:** Conceptualization, Data Curation, Project Administration, Supervision, Validation, Writing – Review & Editing. **R.F.COELHO:** Conceptualization, Data Curation, Formal Analysis, Funding Acquisition, Project Administration, Resources, Validation, Visualization, Writing – Original Draft, Writing – Review & Editing.

PLAGIARISM POLICY

This article was submitted to the similarity system provided by Crossref and powered by iThenticate – Similarity Check.

DATA AVAILABILITY

The data used in this research is available in the body of the document.

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BIOGRAPHIES

Thiago Fonseca Rech received his B.E and M.E degree in Electrical Engineering in the Federal University of Santa Catarina (UFSC) in 2019 and 2021. Currently, is pursuing a PhD degree at the Power Electronics Institute (UFSC) in Florianópolis, Brazil. His main research interests are power electronics systems, optimization theory and renewable energy.

Tailan Orlando was born in Campinas do Sul, RS, Brazil, in 1996. He received the B.S. degree in electrical engineering in 2019 from the Integrated Regional University of Alto Uruguai and Missions (URI), Erechim, Brazil, and the M.S. degree in electrical engineering, in 2022, from the Federal University of Santa Catarina (UFSC), where he is currently working toward the Ph.D. degree at the Power Electronics Institute. His research interests include grid-connected systems, active filters, and green hydrogen generation.

André Luís Kirsten was born in Santa Maria, Brazil, in 1986. He received the B.S., M.Sc., and Ph.D. degrees in electrical engineering from the Federal University of Santa Maria, Santa Maria, Brazil, in 2009, 2011, and 2014, respectively. He is currently a Professor with the Department of Electrical and Electronics Engineering at the Federal University of Santa Catarina (UFSC), Florianópolis, Brazil. His main research interests are: power electronics, digital control, dual active bridge converter and solid-state transformers. Dr. Kirsten is member of the SOBRAEP and IEEE.

Roberto Francisco Coelho was born in Florianópolis, SC, Brazil, in 1982. He received the B.S., M.S., and Ph.D. degrees in electrical engineering from Federal University of Santa Catarina (UFSC), Florianópolis, SC, Brazil, in 2006, 2008, and 2013, respectively. He is currently an Adjunct Professor with the Department of Electrical and Electronics Engineering, UFSC. His research interests include power converters, control, maximum power point tracker systems, grid-connected systems, and distributed generated systems. Dr. Coelho is member of the SOBRAEP and IEEE.