DESIGN AND OPTIMIZATION APPROACH FOR THE MAGNETIC COMPONENTS OF A FIVE-LEVEL CURRENT SOURCE RECTIFIER

Lisandra K. Ries¹, Marcelo L. Heldwein²

¹Instituto Federal de Santa Catarina, Florianópolis – Santa Catarina, Brasil
²Universidade Federal de Santa Catarina, Florianópolis – Santa Catarina, Brasil
e-mail: lisandra.ries@ifsc.edu.br, marcelo.heldwein@ufsc.br

Abstract - This work presents a design and optimization method of the five-level current source converter magnetics components, this is a contribution of this work, since the magnetics volume information of this topology lacks in the literature. The magnetics components are: the interphase transformers, the input and the output filter inductors. The converter volume is minimized when the magnetic components multi-domain optimization is done. The inductance values are defined with given constraints such as Electromagnetic Compatibility standards for differential mode filters, conduction and switching losses of the semiconductors and maximum current variation. This optimization is shown for two different converter switching frequencies: 45 kHz and 150 kHz. The copper losses, iron losses and temperature rise are calculated and used for selecting the smallest volume of each magnetic component with given specifications. A 6-kW prototype with the optimized magnetics was constructed.

Keywords – CLC filter optimization, Current-source converter, DC output filter optimization, Interphase transformer optimization, Magnetics multilevel converter optimization.

I. INTRODUCTION

In this work the three-phase ac-dc current source rectifier (CSR) is assumed in an electric vehicle battery charger application [1]. In a 3-level CSR the output inductor is typically the bulkier component of the converter because it stores a largest amount of energy and inductors tend to have a relatively lower energy density [J/dm³] than capacitors. However, other passive devices are important to the CSR operation and must be properly specified and designed. Capacitors are used at the dc-port of the CSR to establish a low ripple dc voltage. They are manufactured by automated and standardized processes in industry and are commercially available in a fairly large range of specifications, from which temperature, voltage, current, capacitance and life cycle are the most relevant for power electronics applications. A CSR also requires an input filter placed at its ac-port, which limits the propagation of high frequency conducted electromagnetic emissions (CEEs) to the ac power source. Among the filter components of a typical CSR there are inductors and capacitors. Even though 3-level CSRs present desirable electrical characteristics, they are typically identified

as not being the most compact converters. This comes from two main facts, namely: (i) they use two power semiconductors in series to form each switch, leading to increased conduction losses and cooling systems; and, (ii)the passive components are rather bulky. The first issue is being tackled by power semiconductor manufacturers that are researching on devices with low forward conduction voltage with bidirectional capabilities [2], [3]. The second issue can be improved with higher operating frequency and multilevel waveforms, as in a multilevel CSR.

A CSR with a higher number of levels can be used to reduce the passive components dimensions and 5-level CSRs are reported as an attractive solution [4]. It is possible to use two 3-level CSRs connected in parallel to build a 5level CSR having, thus the 3-level CSR as its building block as seen in [5]-[8]. Another option to reduce size is to use interphase transformers (IPTs) at the 3-level CSRs dc-ports to limit the circulating current among them in the 5-level structure [1], [5], [9]-[11]. This power structure is seen in Figure 1 and its operation is reported in [12]. The advantage of interphase transformers over two loosely coupled inductors or two independent inductors is that the IPTs cores ideally are only subjected to ac flux and the component does not store energy. Thus, they can be advantageously built with ferrite cores and operate at relatively high frequency. The output inductors filter the resulting voltage, which presents voltage levels with reduced amplitude and higher effective frequency due to the IPTs. The output inductors need to store energy and, thus, the main options for their core materials are cores with distributed gap such as iron powder and cores with air gap, such as ferrite, amorphous and nanocrystalline. In this context, the magnetic components of a IPTs-based 5-level CSR to be custom designed are the interphase transformers, the output inductor and the filter inductors. Since the specific design of components for the 5-level CSR lacks in the literature, a procedure for their specification and design is proposed in this work. The procedure accounts for the system level influence of the passive components on the electrical, energetic and thermal performances of the converter.

The designs of the interphase transformers, ac-port filter and output inductors can be formulated as multi-domain optimization problems similar to the one reported in [13] for voltage source converters. The magnetic components optimization is a fundamental step in reducing a power converter volume [14]–[16]. In the IPTs case it is related to the transverse or circulating currents minimization and to the semiconductors switching frequency and generated losses. The ac-port filter design case depends mainly on the filter parameters selection in order to comply to electromagnetic

Manuscript received 05/20/2021; first revision 09/21/2021; accepted for publication 10/06/2021, by recommendation of Editor Demercil de Souza Oliveira Jr. http://dx.doi.org/10.18618/REP.2021.4.0018



Fig. 1. Five-level CSR power circuit schematics including its main components, where C_{dc} is the dc-port capacitor, T_1 and T_2 are interphase transformers (coupled inductors), L_{dc} is the output inductor, L_F is the ac-port filter inductor, C_F is the ac filter capacitor. Optional positioning of freewheeling diodes are drawn in gray.

compatibility (EMC) standards. The output filter selection is related to the output current ripple amplitude and associated losses. Another important parameter is the definition of appropriate inductance values for the different magnetic components to satisfy the imposed operation constraints.

Two types of core are considered in the proposed optimization, namely E-shaped and toroidal cores. For the interphase transformer the optimization is exemplarily performed with E-shaped cores. This choice is justified by the possibility of using a core with lower magnetic induction. This is used to demonstrate the need to balance the IPTs currents since unbalance in the currents leads to the transverse mode flow to reach higher values. Current balance control is required so that these values do not reach the saturation. In addition, the magnetic flux in these components is ideally alternate at the switching frequency, i.e., no dc level is expected and the main frequency components are at relatively high frequencies. Thus, E-type ferrite cores are well suited. Toroidal cores made of iron powder material are used for ac- and dc-side filter inductors. In this case, the cores are subjected to relatively high dc or low frequency flux levels, while the superimposed high frequency ripple is relatively small. Thus, the higher flux saturation levels of iron powder materials typically lead to smaller inductors.

A steady state thermal model for the designed inductors is employed in order to estimate the temperature rise and these models were included in the optimization of the designed components. Material properties parameters are modified according to the computed operating temperature. The temperature influence on iron losses are small for the iron powder cores and are, thus, neglected.

The optimization uses current and induction curves obtained from the simulation for the converter operation worst cases at two different switching frequencies, namely: 45 kHz and 150 kHz. The switching frequency choice is latter justified by the optimization results. For the definition of the optimal frequency for passive components volume minimization, the reader should apply the presented methodology scanning a design space where the switching frequency is the main parameter to vary within a set range. A design is exemplarily discussed throughout the paper to illustrate the proposed procedure and experimental results from a 6-kW 5-level CSR prototype that uses the designed components are presented to validation.



Fig. 2. Simulated differential mode voltage u_{dm} assuming the minimum input voltage condition without an input filter for the following switching frequency cases: (a) 45 kHz; and, (b) 150 kHz.

II. INDUCTANCE VALUES DEFINITION

The optimization of the magnetic components should be performed considering the worst case based on the design specifications. This section discusses how to specify the required inductance values for each of the magnetic components based on EMC requirements for the filter inductors, impact on semiconductor losses for the interphase transformers and ripple current for the output inductor.

A. Modulation Index

Considering that the mains line-to-line voltage, $U_{g,l-1}^{rms}$, varies between two extreme values, exemplarily $U_{g,l-1,\min}^{rms}$.. $U_{g,l-1,\max}^{rms} = 342$ V..418 V, the modulation index, M, varies accordingly between a minimum and a maximum value in the range M_{\min} .. $M_{max} = 0.742..0.907$. This defines the operation range of the converter.

B. Differential Mode Input Filter Inductance

The design of the input filter is performed taking into account Electromagnetic Compatibility (EMC) standards for differential mode filters. CISPR Class B levels for quasi-peak (QP) are used. The differential mode (DM) voltage is assumed symmetric and is calculated for phase *a* as

$$u_{\rm dm} = \frac{2}{3}u_{\rm an} - \frac{1}{3}u_{\rm bn} - \frac{1}{3}u_{\rm cn}.$$
 (1)

A simulation of the 5-level current source converter operating at full load without an input filter is performed. Circuit simulation is used because the time behavior of the involved electric quantities is highly complex. In addition, the defined EMC standards measurement procedure leads to non-linear behaviors and, thus, time consuming even for computer simulations. The differential mode voltage is measured during one grid period T_g for the minimum input voltage condition and for the switching frequencies of $f_{sw} = 45$ kHz and $f_{sw} = 150$ kHz (Figure 2).

The spectrum of the DM signal and the estimated quasipeak values for the frequency range of 9 kHz to 1.5 MHz is seen in Figure 3. The employed simulation software is GECKOCircuits [17] and the QP estimation is made within the software.

The attenuation required by the differential mode filter can be calculated with

$$Att_{\text{req,dm}}(f_{\text{int}}) = 20 \cdot \log\left(\frac{U_{\text{dm}}(f_{\text{int}})}{1\mu V}\right) - Limit_{\text{ClassB}}(f_{\text{int}}) + \text{Margin.}$$
(2)

from the difference of the quasi-peak value and the limit value at the interest frequency (f_{int}) , which is the first multiple



Fig. 3. Predicted harmonic spectra and quasi-peak detection value estimations (QP) for the DM voltage conducted emissions assuming the worst case without input filter for the switching frequencies: a) 45 kHz; and, b) 150 kHz.



Fig. 4. Filter that can be used to fulfill the electromagnetic compatibility standard EN55022 employed in the 5L-CSR.

of the switching frequency that is within the standard specified range. For the 45 kHz and 150 kHz switching frequencies the interest frequency are, 180 kHz and 300 kHz, respectively. A margin of 6 dB is suggested [18].

The chosen input filter topology is a two-stage *LC* filter with capacitor series damping as in Figure 4. Filter attenuation is chosen to be higher in the first stage of the filter, $Att_{n1}(f_{int})$, to simplify the dynamic behavior, resulting in a lower cut-off frequency. A 40 dB higher attenuation is suggested [18]. Thus,

$$Att_{n1}(f_{int}) = \left(\frac{Att_{req,dm}(f_{int}) + 40}{2}\right),$$

$$Att_{n2}(f_{int}) = Att_{req,dm}(f_{int}) - Att_{n1}(f_{int}),$$
(3)

where $Att_{n2}(f_{int})$ is the attenuation of the second filter stage. The first stage cut-off frequency, f_{c1} , is typically set at a decade below the second stage cut-off frequency, f_{c2} , so that,

$$f_{c1} = \frac{f_{int}}{\sqrt{10} \left(\frac{Att_{n1}(f_{int})}{20}\right)}, \ f_{c2} = \frac{f_{int}}{\sqrt{10} \left(\frac{Att_{n2}(f_{int})}{20}\right)}.$$
 (4)

The following sequence of calculations [19] is performed for the choice of filter parameters and the combination that results in a lower volume is selected:

1. The variation of the peak-to-peak input voltage across the capacitor, $\Delta U_{\text{CF,max}}$, is limited between 1%...8%. Thus,

TABLE ITwo-Stage Input Filter Parameters Selection for the45 kHz and 150 kHz Switching Frequencies, Using ClassX2 Capacitors

$f_{\rm sw}[\rm kHz]$	$C_1[\mu F]$	$L_1[\mu H]$	$C_{1d}[\mu F]$	$r_{1d}[\Omega]$	$C_2[nF]$	$C_{2d}[nF]$	$r_{2d}[\Omega]$
45	4.7	417.7	4.7	15	470	470	15
150	4.7	286.6	4.7	12	270	270	22

 C_1 is calculated with

$$C_1 = I_{\rm dc} M_{\rm max} \frac{(1 - M_{\rm max})}{\Delta U_{\rm CF,max} 2 f_{\rm sw}}.$$
(5)

2. Inductance L_1 is

$$L_1 = \frac{1}{4\pi^2 f_{\rm c1}^2 C_1}.$$
 (6)

- 3. Capacitor C_{1d} is computed assuming a damping factor of n = 1. Thus, $C_{1d} = C_1 \cdot n$.
- 4. The damping resistor r_{1d} is computed with the optimum damping factor assuming only the first filter stage that gives

$$r_{\rm 1d} = \sqrt{\frac{L_1}{C_1}} \sqrt{\frac{(2+n)(4+3n)}{2n^2(4+n)}}.$$
(7)

5. The second filter stage is calculated considering an internal grid inductance of 50 μ H. Its capacitance can be calculated with

$$C_2 = \frac{1}{4\pi^2 f_{\rm c2}^2 L_{\rm g}}.$$
 (8)

- 6. The other components of the filter, such as C_{2d} and r_{2d} , are calculated using the same expressions used in the first stage.
- 7. The total volume of the filter is calculated with the volumetric coefficients of the film capacitors, $k_{\rm C}$, and inductors, $k_{\rm L}$,

$$Vol = k_{\rm C} (C_1 + C_2 + C_{\rm 2d}) U_{\rm C1rms}^2 + k_{\rm L} L_1 I_{\rm L1rms}^2,$$
(9)

which are assumed as in [19].

8. The power factor angle, δ , should be limited. It is assumed lower than $\delta = 5^{\circ}$ so that the expression $\delta \leq \tan^{-1}\left((C_1 + C_{1d} + C_2 + C_{2d})\omega_g \frac{\hat{U}_{g,max}}{\hat{I}_{g,max}}\right)$ holds. The input filter self-inductance value L_1 is defined after

The input filter self-inductance value L_1 is defined after applying the previous volume minimization sequence. For the switching frequencies of 45 kHz and 150 kHz the optimization results are summarized in Table I. Interestingly, the resulting first stage capacitance values for minimum volume filters are similar. However, inductances and second stage capacitances are reduced for the higher frequency specification.

Figure 5 shows the attenuation curves achieved using the calculated input filter. The Class B limits are fulfilled for the two switching frequencies.

The effect of the dc component should be taken into account in the input filter design, since the 60 Hz waveform is a considerable low frequency when compared to f_{sw} . The minimum value of the inductances need to be guaranteed at the peak value of the ac current at the worst condition.

The differential mode voltage is estimated again with the insertion of the input filter as shown in Figure 6. The resulting waveform is highly attenuated when compared to Figure 2. It presents predominantly low frequency components.



Fig. 5. Predicted harmonic spectra and the computed quasi-peak detection for the differential mode voltage conducted emissions for the worst case after using the input filter. The CISPR Class B standard is met for both switching frequency cases: a) 45 kHz; and, b) 150 kHz.



Fig. 6. Simulated differential mode voltage with minimum input voltage condition, with input filter and for the following switching frequency cases: (a) 45 kHz; (b) 150 kHz.



Fig. 7. Composition of the currents in a semiconductor from the longitudinal and transverse mode current components. Note that this a waveform simulation, bringing the details of the semiconductor within a switching period.



Fig. 8. On-state resistance and switching energies considering the SiC MOSFET C3M0075120K available from Cree.

TABLE II Conduction and Switching Losses Considering the Input Voltage Variation 418 V..380 V..342 V and Switching Frequencies of 45 kHz and 150 kHz

		45 kHz			150 kHz	5
	M_{\min}	М	M _{max}	$M_{\rm min}$	М	M _{max}
$P_{\rm cond}$ [W]	55.2	58.5	62.4	55.2	58.5	62.4
$P_{\rm com}$ [W]	12.3	11.1	10.0	40.9	37.1	33.4
P_{total} [W]	67.5	69.6	72.4	96.1	95.6	95.8

C. Interphase Transformer

The choice of the self inductance value of the interphase transformers (IPTs) is done here based on the influence it has on conduction and switching losses of the semiconductors. The current through each winding can be split into two components, namely: (i) a longitudinal mode component, $i_{\rm I}$; and, (ii) a transverse mode one, $i_{\rm T}$, [20]. The transverse mode current rate of change is modified by varying the voltages applied to the IPTs windings, i.e., u_{pp} and u_{nn} . This current component flows only through the semiconductor devices and, thus, impacts their conduction and switching losses. Figure 7 shows an example of the current composition in one of the semiconductors of the 5-level CSR, where the longitudinal and transverse mode currents of the interphase transformer form, for instance, the first winding current $(i_{p,1})$, i.e., $i_{p,1} =$ $i_{\rm L} + i_{\rm T}$. The current in the semiconductor $i_{S_{\rm i,pn}}$ equals the current $i_{p,1}$ at the time intervals when it conducts. The currents in the windings of one of the IPT, $i_{p,1}$ and $i_{p,2}$, present ripple frequency mainly at $2f_{sw}$, as do i_L and i_{dc} . The ripple of the current i_{dc} is mainly limited by the output inductance and also affects the semiconductors losses value, because the ripple is proportional to the longitudinal mode current, which also circulates in the semiconductors. The ripple frequency of the currents through the switches and of the transverse mode current is f_{sw} .

The influence of the transverse mode current change rate on the conduction and switching losses is verified with simulations from GECKOCircuits software. A loss energy table showing the expected turn on and turn off values is created [21] with loss information from the device datasheet (Figure 8). The calculated results for both switching frequencies are summarized in Table II. It is verified that the switching losses influence are more relevant at 150 kHz frequency.

The highest overall loss occurs with the minimum modulation index case. Thus, this is used in the further simulations, where the IPT inductance value is varied and the resulting losses in the semiconductors are evaluated. Figure 9 presents the results for both switching frequencies. The



Fig. 9. Interphase transformer self-inductance value choice for the 45 kHz and 150 kHz switching frequencies, considering the losses in the semiconductors.

simulated results was close to the calculated value, which does not vary with the inductance value since its calculation does not consider, neither, the rate of change of the output current, nor the transverse mode currents. When the inductance is reduced the rate of variation of the transverse mode current increases and, thus, the losses increase. The chosen values limit the transverse mode current variation rate by 0.8 A. The inductances of 1.55 mH and 470 μ H are chosen for the frequencies of 45 kHz and 150 kHz, respectively, since further increasing their values has almost no effect on reducing losses.

D. Output Filter Inductance

A predominantly dc current is required for the converter to operate as expected. Thus, a limited ripple is required. The output current rate of change is maximum for the case of lower modulation index. The output filter is designed considering that the maximum current variation is $\Delta i_{Ldc}^{pp} \leq 30\%$ and using

$$L_{\rm dc,min} = \frac{U_{\rm dc}}{\Delta i_{\rm L_{dc}}^{\rm pp} 2f_{\rm sw}} (1 - M_{\rm min}). \tag{10}$$

In practice this inductance also limits the total current ripple in the semiconductors since the output current is proportional to the longitudinal mode current, which also circulates through the semiconductors (see Figure 7). This effect has less influence on the semiconductors losses because the distribution of the longitudinal portion is balanced among all the semiconductors, differently from the transverse portion.

The specified inductance values for the output filter are: $L_{dc} = 230\mu$ H and $L_{dc} = 70\mu$ H for 45 kHz and 150 kHz switching frequencies, respectively. The effect of the dc component is taken into account in the output filter design.

III. GEOMETRIC PARAMETERS

Cores with EE and toroidal geometries are used in the magnetic components design. Their parameters are defined in Figure 10. Other relevant data from the manufacturers datasheets are the core cross-sectional area A_e , the effective magnetic mean path length l_e and the core volume V_e .

Copper wires are used for the construction of the windings. If the winding has more than one wire, the Litz wire structure is considered. A four-wire winding in parallel is shown in Figure 11. In this case the wire thickness d_i will be modified by the factor $\sqrt{k} = 2$. The copper wire thickness neglecting its insulation is represented by d_b .



Fig. 10. Main dimensions for the considered core geometries: a) EE; and, b) Toroid.



Fig. 11. Considered winding structures based on round copper wires and their representation when considering k wires in parallel: a) single wire; and, b) four parallel wires and equivalent structure.

a)

The mean length turn, l_t , is calculated with the mean between the maximum and minimum values that one turn can occupy in the EE core and in the toroidal core. Thus,

$$l_{t} = 2D1 + D3 + 2l_{C} + D2 - 2D4, \quad (EE)$$

$$l_{t} = 2D3 + D1 - D2/2, \quad (Toroid). \quad (11)$$

where the coil former or insulation thickness is $l_{\rm C}$.

The winding factor K_w represents the fraction of the core window area A_w that is filled by N copper wires. It is calculated according to

$$K_{\rm w} = \frac{N\pi d_{\rm i}^2 k}{2(D2 - D3 - 2D4 - 2l_{\rm c})(D5 + D6 - 2D7 - 2l_{\rm c})}, \text{ (EE)}$$
$$K_{\rm w} = \frac{Nd_{\rm i}^2 k}{D2^2}, \text{ (Toroid)}. \tag{12}$$

and the maximum winding factors are 60% for the EE geometry and 40% for the toroids.

The maximum number of conductors per layer within the window area of the EE core can be found with

$$n_{\max,l} = \operatorname{floor}\left(\frac{D5 + D6 - 2D7 - 2l_{\rm C}}{d_{\rm i}\sqrt{k}}\right).$$
(13)

and the estimated number of layers is given by

$$n_{\rm l} = \frac{N}{n_{\rm max,l}}.$$
 (14)

Figure 12.a represents the arrangement of conductors within the window area for an EE core. The maximum number of conductors per layer is an integer number, so the floor function is used, which rounds down the specified expression.

For the toroidal geometry the maximum number of conductors per layer can be estimated with

$$n_{\max,l} = \text{floor}\left[\pi\left(\frac{D2}{d_i\sqrt{k}} - (2l-1)\right)\right], \ l \ge 1,$$
(15)

where the corresponding layer is given by l.

The process of calculating the number of layers is iterative. The procedure considers that the first layers are fully occupied. Then the amount of conductors arranged in the last layer is



Fig. 12. Representation of the layers number calculation considering the geometries: a) EE; and, b) toroidal.

estimated and the proportion of conductors that last layer is finally computed. Figure 12.b represents the arrangement of conductors within the window area for a toroid. In the first layer $n_{\max,1} = 14$ and in the second and last layer $n_{\max,2} = 8$. Only six conductors are part of the last layer, the number of layers for this condition is $n_1 = 1 + 6/8 = 1.75$.

IV. MAGNETIC PARAMETERS

The magnetic parameters of the considered cores define saturation and core loss behaviors. For example, the main magnetic parameters for the core E55/28/25 built with material N87 from Epcos working with magnetic field H = 250 A/m, frequency f = 10 kHz and temperature $T = 100^{\circ}$ C are: inductance factor $A_{\rm L} = 7300$ nH (+30/-20%), effective relative permeability $\mu_{\rm e} = 1740$ (+30/-20%) and saturation induction $B_{\rm S} = 310$ mT. This data is temperature dependent. and is exemplarily used to define the chosen operation point.

The minimum self-inductance value $L_{p,min}$ is specified for a given operating point and the number of turns required for this realization is calculated with

$$N = \sqrt{\frac{L_{\rm p,min}}{A_{\rm L,min}}},\tag{16}$$

where $A_{L,\min}$ is within the given tolerance range.

The voltage waveform applied to the transformer primary and the inductor windings are drawn from the converter simulation for the worst case scenario. The induction waveform is derived from this voltage with

$$B(t) = \frac{1}{A_{\rm e}N} \int_0^T v(t) \, dt.$$
(17)

Thus, the induction waveform can be obtained with the geometric and magnetic core parameters. The different induction waveforms are shown in Figure 13. The induction is specified not to exceed 90% of the saturation limit for the core being evaluated within a possible optimal result.

A. Steinmetz Coefficients

The Steinmetz coefficients are used for core losses calculation and must be obtained for each operation point. For each given operating frequency, peak induction and temperature, the obtained Steinmetz coefficients result in the volumetric losses extracted from the core material loss graphs. Figure 14 presents the volumetric loss plot for the temperature of 100°C for the N87 material. The Steinmetz equation at frequency f_{sw} , peak induction \hat{B} for the temperature of 100°C is defined with

$$P_{\rm V} = k_{\rm s} f_{\rm sw}^{\alpha} \hat{B}^{\beta}, \qquad (18)$$

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Fig. 13. Induction waveforms for magnetic components of the converter, where B_{L_1} is for the input filter inductor, $B_{L_{px}}$ is for the magnetization of one of the interphase transformers and $B_{L_{dc}}$ is for the output inductor. The time axis presents one third of the mains period, which is enough to represent the repetition of the waveforms.



Fig. 14. Volumetric losses plot as a function of the frequency for different magnetic induction peak values and temperature of 100°C for the N87 material.

where quadratic minimization methodology can be used to obtain the Steinmetz coefficients [22]. A ρ matrix of order $i \times 3$ is synthesized from the volumetric losses data as in Figure 14. Thus,

$$\rho = \begin{bmatrix} f_{\text{sw1}} & \hat{B}_1 & P_1 \\ \vdots & \vdots & \vdots \\ f_{\text{swi}} & \hat{B}_i & P_i. \end{bmatrix}$$
(19)

Matrices \mathbf{A}_{ρ} , \mathbf{W} and \mathbf{b}_{ρ} are used in the minimization, where A_{ρ} has order $i \times 3$, \mathbf{W} is a $i \times i$ diagonal matrix and vector \mathbf{b}_{ρ} has order $i \times 1$. Their elements are defined with

$$A_{\rho_{i,1}} = 1, A_{\rho_{i,2}} = \log(\rho_{i,1}), A_{\rho_{i,3}} = \log(\rho_{i,2}),$$
(20)

$$W_{\rm i,i} = \frac{\varepsilon_1}{(|f_{\rm sw} - \rho_{\rm i,1}| + \varepsilon_2)(|\hat{B} - \rho_{\rm i,2}| + \varepsilon_3)}, \tag{21}$$

where $\varepsilon_1 = 10$, $\varepsilon_2 = 10^{-2}$ and $\varepsilon_3 = 10^{-2}$, and $\mathbf{h}_2 = \log(\alpha_{12})$

$$b\rho_{i,1} = \log(\rho_{i,3}).$$
 (22)

The result vector λ , of order $i \times 1$, gives the desired coefficients and is given by

$$\boldsymbol{\lambda} = [\mathbf{A}_{\boldsymbol{\rho}}^{T} \mathbf{W} \mathbf{A}_{\boldsymbol{\rho}}]^{-1} \mathbf{A}_{\boldsymbol{\rho}}^{T} \mathbf{W} \mathbf{b}_{\boldsymbol{\rho}} = [\log(k_{s}) \ \boldsymbol{\alpha} \ \boldsymbol{\beta}]^{T}.$$
(23)

With the Steinmetz coefficients for various operation points, the volumetric losses curve can be drawn as a function of the magnetic induction and frequency for the temperature of 100° C as in Figure 15. This procedure can be repeated for other temperatures to account for temperature dependent characteristics.

V. COPPER LOSSES

The copper losses can be modeled through equivalent resistances of the windings. The dc resistance is defined by



Fig. 15. Volumetric losses plot as a function of the magnetic induction and frequency for the temperature of 100°C using the described quadratic minimization procedure for the N87 Epcos material.

the conductors geometry, resistivity, length and temperature. The resistance per unit length is found with

$$\frac{R_{\rm dc}}{l_{\rm b}} = \frac{1}{\sigma(T_{\rm f})A_{\rm b}},\tag{24}$$

where $R_{\rm dc}$ is the dc resistance $[\Omega]$, $l_{\rm b}$ is the winding length [m], σ is the material conductivity, $T_{\rm f}$ is the operating temperature and $A_{\rm b}$ is the copper area. However, the operation of the magnetic components occurs in a large frequency range and skin and proximity effects must be included in the resistance model.

For the cases where the core does not present a relevant air gap Dowell's equation for round wires can be used [23]. This gives an estimation of the ac resistance $R_{ac,n}$ considering, both, skin and proximity effects. The expressions for the ac resistance are

$$R_{\rm ac,n} = R_{\rm dc} F_{\rm r,n}, \ A_{\rm n} = \frac{\pi}{4}^{3/4} \frac{d_{\rm b} \sqrt{\eta_{\rm b}}}{\delta_{\rm n}},$$

$$F_{\rm r,n} = A_{\rm n} \left[\frac{\sinh(2A_{\rm n}) + \sin(2A_{\rm n})}{\cosh(2A_{\rm n}) - \cos(2A_{\rm n})} + \frac{2}{3} (n_{\rm l}^2 k - 1) \frac{\sinh(A_{\rm n}) - \sin(A_{\rm n})}{\cosh(A_{\rm n}) + \cos(A_{\rm n})} \right].$$
(25)

where *n* is the current harmonic index, $F_{r,n}$ is the ac resistance factor for the *n*th harmonic, δ_n is the copper penetration depth at the operating temperature for the *n*th harmonic, η_b is the porosity factor and *k* is the number of conductors in parallel.

Dowell's equations are an appropriate equation to be used in optimization routines. The result can be latter refined through Finite Element simulations. If an air gaped core is used, other considerations must be made. It is not the case of this work because the EE core used in the interphase transformer does not have air gap. For the input and output filters, toroidal cores made with iron powder are used.

A. Current Spectra

The characteristic waveforms for each magnetic component is seen in Figure 16. In the input filter the current i_a is mainly sinusoidal at the frequency of 60 Hz. Interphase transformers are considered to be balanced, i.e., $i_{p,1} = i_{p,2}$ and $i_{n,1} = i_{n,2}$. The difference between the currents $i_{p,1}$ and $i_{p,2}$ shown in Figure 16 can be undestood by knowing that a simulation step is never small enough to eliminate such difference and that it depends on the simulation initial conditions.

The current harmonic spectra are seen in Figure 17. For the input filter only the 60 Hz component is considered, since the upper frequencies do not present significant amplitude, contributing very little to the losses calculation. For the interphase transformer and output filter, the GECKOCircuits simulation tool is used to define frequency spectra up to



Fig. 16. Steady state waveforms of the currents: (a) at phase *a* of the input filter i_a ; (b) at the output filter inductor I_{dc} ; and, (c) in the interphase transformer windings $i_{p,1}$ and $i_{p,2}$.



Fig. 17. FFT of currents: a) I_{dc} (output filter) and b) $i_{p,1}$ (interphase transformer).

4 MHz. The transformer harmonic spectra is assumed similar for each winding. The largest current amplitude is dc, followed by multiple components of the switching frequencies. The output inductor has a dominant dc component. On the dc filter side the current has components that are multiples of $2f_{sw}$.

B. Copper Losses Calculation in the Optimization Routine

The copper loss is calculated if the presence of all current harmonic components with

$$P_{\rm W} = R_{\rm dc} \sum_{n=1}^{\infty} F_{\rm r,n} I_{\rm n,rms}^2.$$
 (26)

Due to the high switching frequency, the cost of calculating the previous equation is very high in an optimization routine. It is considered that the current harmonic spectra is limited to 4 MHz. For the copper losses calculation, only the contribution of harmonic groups centered on multiple harmonics of the switching frequency is considered. Thus,

$$P_{\rm W} = R_{\rm dc} \sum_{j=1}^{j_{\rm max}} \left[\sum_{n=n_{\rm j}-100}^{n_{\rm j}+100} F_{\rm r,n} I_{\rm n,rms}^2 \right], \ n_{\rm j} = j \frac{f_{\rm sw}}{f_{\rm g}}.$$
 (27)

VI. CORE LOSSES

The Steinmetz equation (equation (18)) is used to calculate the volumetric losses P_V given a sinusoidal excitation of peak induction \hat{B} and frequency f_{sw} . The major limitation of this equation is that it is only correctly applied to sinusoidal excitations. Figure 13 presents the different induction waveforms of the magnetic components. It is concluded that only the input filter has a sinusoidal induction waveform. The induction peak-to-peak variation ΔB of the interphase transformer and of the output filter is different for each interval in between on switching period T_{sw} . The iGSE method (Improved Generalized Steinmetz Equation) improves the Steinmetz equation by considering that the losses from the domain walls movement are also related to the rate of change of induction dB/dt and that smaller hysteresis loops are considered [24]–[26]. It can be applied to non sinusoidal waveforms. Thus, the volumetric loss is calculated here with the iGSE method and is given by

where

$$k_{\rm i} = \frac{k}{(2\pi)^{\alpha - 1} \int_0^{2\pi} |\cos(\theta)|^{\alpha} 2^{\beta - \alpha} d\theta}.$$
 (29)

(28)

Parameters k, α and β are the same used in the original Steinmetz formulation (equation (18)). Considering that the waveforms in Figure 13 are piecewise linear the energy per volume E_1 dissipated at each switching period within the period $T_g/3$ is

 $P_{\rm V} = \frac{1}{T} \int_0^T k_{\rm i} \left| \frac{dB}{dt} \right|^{\alpha} (\Delta B)^{\beta - \alpha} dt,$

$$E_{\rm l} = \frac{k_{\rm i,l} (\Delta B_{\rm l})^{\beta_{\rm l} - \alpha_{\rm l}}}{(A_{\rm e}N)^{\alpha_{\rm l}}} \sum_{n=1}^{6} v^{\alpha_{\rm l}} (\Delta t_{\rm n}) \Delta t_{\rm n}, \tag{30}$$

where the index l indicates the l^{th} switching period, the index n indicates the n^{th} interval of one switching period and v is the voltage waveform applied to the transformer primary or the inductor windings. In the case of the output inductor, the switching frequency must be updated to $2f_{\text{sw}}$.

From the resulting expression, it is concluded that the Steinmetz coefficients are to be updated for each switching period since the peak induction varies. Voltage v is also modified for each interval Δt_n within a switching period. The average volumetric power dissipated by the core is calculated by

$$P_{\rm V} = \frac{3}{T_{\rm g}} \sum_{l=1}^{T_{\rm g}/(3T_{\rm sw})} E_{\rm l}.$$
 (31)

Finally, the power dissipated in the core, $P_{\rm C}$, is given by

$$P_{\rm C} = V_{\rm e} P_{\rm V}. \tag{32}$$

VII. TEMPERATURE RISE

A simple equation is widely used as a reasonably accurate method to predict the temperate rise [27] in magnetic components with natural convection. With this, the temperature rise $\Delta T_{\rm E}$ is estimated with

$$\Delta T_{\rm E} = 450 \left[\frac{(P_{\rm W} + P_{\rm C})}{10^4 A_{\rm t}} \right]^{0.826},\tag{33}$$

where $P_{\rm C}$ [W] is the power loss of the core, $P_{\rm W}$ [W] is the power loss of the winding and $A_{\rm t}$ [m²] is the effective surface area available to exchange heat with the environment. To correctly use this equation the transformer is ideally homogeneous, meaning that the thermal energy is dissipated uniformly through the surface area of the core and winding. The major flaw of this equation is that in an optimization result there may exist a large variation between the distribution of the core and winding losses.

To overcome this limitation, models for the temperature rise calculation must be created, both for EE ferrite core and for toroidal cores [28], [29]. Traditional studies of the toroidal thermal circuits consider uniform windings around the core, but an uneven winding distribution must be considered for a correct prediction of the temperature rise, since the conductor spacing is thicker on the inside and thinner on the outside [29]. There are two different thermal models, since there are two different main structures. It is possible to obtain the steady state temperature rise, by knowing the geometric parameters, the initial temperature T_i , the operating temperature T_f , the thermal characteristics information and core and winding losses.

VIII. OPTIMIZATION METHODOLOGY

The optimization flowchart is presented in Figure 18. The first step of optimization is to define the magnetic inductance value and design specifications. From the geometric information one can verify if a given structure is feasible. The magnetic parameters, such as saturation induction values, the relative permeability and the Steinmetz parameters are updated. The Steinmetz parameters are frequency, induction and temperature dependent, i.e., they are modified at each operating point. The objective of the optimization is to choose the lowest possible volume respecting the maximum temperature of $T_{\rm f,max} = 100^{\circ}$ C. The temperature rise $\Delta T_{\rm C}$ is calculated as a function of copper and iron losses using the thermal modeling of the magnetic components. These losses are temperature dependent, so some interactions must be made until the specified convergence of 0.01% is reached.



Fig. 18. Magnetic components optimization flow chart.

IX. OPTIMIZATION RESULTS

The objective of this section is to present the optimization results using the presented methodology. The following magnetic components are optimized:

1. Input filter inductor: The induction waveform of this

component has the characteristic of a sinusoid with a 60 Hz frequency. The employed core is a toroidal iron powder core of High Flux material (HF) available from Magnetics due to its high saturation value;

- 2. Interphase Transformer: Ferrite E core is used with the materials N27, N41, N49, N72, N87, N92 and N97 (avialable from TDK). Its induction characteristic waveform has fundamental frequency of $3f_g$ and other harmonic components at multiples of this frequency and f_{sw} .
- 3. Output inductor: Its current presents harmonic components at dc and multiples of $2f_{sw}$. Magnetics toroidal iron powder core is also used. The considered materials are High Flux (HF, $B_S = 1.5$ T), Kool Mu (KM, $B_S = 1.0$ T) and Molypermalloy Powder (MPP, $B_S = 0.75$ T).

Ambient temperature is assumed at 40° C and the worst cases are considered in the optimization. Table III presents the optimization results for the switching frequencies of 45 kHz and 150 kHz. Results for each material are presented and the result presenting the lower volume is highlighted (bold). The minimum inductance value $L_{p,min}$, number of turns N, iron losses $P_{\rm C}$, copper losses $P_{\rm W}$ and final temperature $T_{\rm f}$ are shown in the table. It is verified that, for 150 kHz, the magnetic components have smaller volume as expected.

TABLE III

Optimization Results for the Worst Operating Case and $T_a = 40^{\circ}$ C Considering the Following Components: (a) Input Filter Inductor; (b) Interphase Transformer; and, (c) Output Inductor

L _{p,min}	Core	AWG	Ν	PC	$P_{\rm W}$	$T_{\rm f}$	Vol	f_{sw}
[µ́H]				[W]	[W]	[°C]	[dm ³]	[kHz]
(a) 417.7	C058110 HF60	17	103	7.34e-3	6.10	101.6	4.70e-2	45
(b) 1550	E55/28/21 N87	12	2x18	0.98	3.37	93.4	5.73e-2	45
	0058617 HF60	10	40	2.07	8.81	93.1	7.50e-2	45
(c) 230	77102A7 KM26	12	94	0.60	17.81	94.2	1.39e-1	45
	C055866 MPP125	10	64	1.23	11.8	93.5	6.55e-2	45
(a) 286.6	C058089 HF125	13	67	2.35e-3	4.32	92.2	2.88e-2	150
(b) 470	E42/21/20 N87	11	2x11	1.59	1.45	95.4	3.14e-2	150
	C058716 HF60	10	36	2.68	5.41	100.9	2.96e-2	150
	77109A7 KM125	10	40	2.20	6.40	95.1	3.72e-2	150
(c) 70	C055087 MPP160	10	30	3.20	4.59	103.2	2.72e-2	150

In order to better compare the compromise between switching frequency and volume, one must also consider the heatsinks volume used in the design. The filter capacitors volume is neglected, since the first stage capacitor of the filter has the same capacitance for the two considered switching frequencies and do not change the volume ratio of the projects (Table I). Heatsinks available from Fischer Elektronik are considered. The smallest heatsink volume that can allocate 6 switches and 7 diodes with TO-247 packages and respect the maximum temperature rise at the junction is the Fischer LAM 3 150 heatsink. Table IV shows the total volume of the magnetic components and heatsinks considering the quantity of each item. The selection of the switching frequency of 150 kHz is justified because a design with smaller volume is achieved.

Although the optimization routine was used on more than 766 Magnetics toroidal cores and 46 Epcos type E cores, the final choice depended on the laboratory availability. In practice the cores available for the construction of

TABLE IV

Comparison of the Total Volume and Power Density Considering the Magnetic Components, Capacitors and Heatsinks for Two Switching Frequencies

		-	-
Component	Quantity	$f_{sw} = 45 \text{ kHz}$	$f_{sw} = 150 \text{ kHz}$
Heatsinks	2	2.70e-1 dm ³	2.70e-1 dm ³
Input filter inductors	3	1.41e-1 dm ³	8.64e-2 dm3
Interphase transformers	2	1.15e-1 dm ³	6.28e-2 dm3
Output inductor	1	6.55e-2 dm ³	2.72e-2 dm3
Input filter capacitors	12	1.58e-1 dm3	1.58e-1 dm3
Output capacitor	1	1.01e-1 dm ³	1.01e-1 dm ³
Total Volume	-	8.49e-1 dm ³	7.05e-1 dm3
Power Density	-	7.06 kW/dm ³	8.51 kW/dm ³
Magnetics Volume	-	3.21e-1 dm ³	1.76e-1 dm ³
Power Density	-	18.69 kW/dm ³	34.02 kW/dm3

TABLE V

Optimization Results Considering the Two Different Operating Points, Switching Frequency of 150 kHz and Ambient Temperature of 40°C: (a) Input Filter; (b) Interphase Transformer; (c) Output Inductor

	Worst operating point for each case							
L _{p,min}	Core	AWG	Ν	$P_{\rm C}$	$P_{\rm W}$	$T_{\rm f}$	Vol	
$[\mu H]$				[W]	[W]	$[^{\circ}C]$	[dm ³]	
(a) 286.6	0078439A7 XF60	18	58	1.51e-2	4.56	95.8	3.35e-2	
(b) 470	E55/28/25 N87	15	2x9	1.50	0.88	72.9	6.69e-2	
(c) 70	C055195A2 MPP125	18	20	3.31	4.39	93.4	3.53e-2	
	Nominal operating point $M = 0.816$							
$L_{\rm p,min}$	Core	AWG	Ν	$P_{\rm C}$	$P_{\rm W}$	$T_{\rm f}$	Vol	
$[\mu H]$				[W]	[W]	$[^{\circ}C]$	[dm ³]	
(a) 286.6	0078439A7 XF60	18	58	1.46e-2	4.36	93.7	3.35e-2	
(1) 170	DEEIDO IDE NIOT			~	o = o	<pre></pre>	6 60 0	
(b) 4/0	E55/28/25 N87	15	2x9	0.77	0.78	61.7	6.69e-2	
(b) 470 (c) 70	E55/28/25 N87 C055195A2 MPP125	15 18	2x9 20	0.77 2.25	0.78 4.29	61.7 86.3	6.69e-2 3.53e-2	
(b) 470 (c) 70	C055195A2 MPP125 Magnetics Volume	15 18	2x9 20	0.77 2.25	0.78 4.29 2.70e-	61.7 86.3 1 dm ³	6.69e-2 3.53e-2	

the converter were the toroidal iron powder 0078439A7 material XF-60 from Magnetics for the input filter, the ferrite core E55/28/25 with material N87 from Epcos for the interphase transformer and the toroidal iron powder from Magnetics C055195A2 material MPP125 for the output filter. Considering an ambient temperature of 40°C and the switching frequency at 150 kHz, the number of turns for each magnetic is calculated according to the minimum inductance specifications. The losses and the final temperature are given in Table V for the worst case and for nominal operation. The magnetic components have higher volume when compared to the results shown in Table III for the 150 kHz switching frequency.

A 6-kW prototype with the optimized magnetics was constructed as seen in Figure 19 with the specifications given in Table VI.

TABLE VI						
5L-CSR Specification						

220 V
380 V
60 Hz
150 kHz
6.0 kW
50 µF
4.7 μF
287 µH
70 µH
$470 \ \mu H$

The converter power density may be defined as the relation of the maximum output power and the total volume of the converter, when considering the converter a boxed



Fig. 19. 6-kW 5-level CSR prototype: (a) top side view; and, (b) bottom view.

volume, as shown in Figure 19, a 1.1 kW/dm³ power density is achieved. However, this study aims to optimize the magnetic components, only this volume must be considered in comparison to previous work: a 7.5 kW/28 kHz 3L-CSR with 11.51 kW/dm³ power density, considering the volume distribution of 12% to magnetics input filter and 34% to output inductor [30]. The 5L-CSR achieved a 22.26 kW/dm³ power density when considering only the magnetics volume. It is important to note that the size of the magnetic components final choice depended on the laboratory availability and if the construction of the converter were made with the magnetics components given in Table IV, the power density would grow to 34.02 kW/dm³.

The 6-kW 5L-CSR prototype was tested at the rated operating point. Figure 20 presents the time behavior of the output voltage u_{dc} , output current i_{dc} , input voltage u_a and input current i_a . Figure 21 shows the time behavior of the interphase transformer currents $i_{p,1}$ and $i_{p,2}$, input voltage u_a and voltages across the transformer windings u_{pp} . Current i_a follows the sinusoidal input phase voltage u_a and thus, a near unity power factor is achieved. The currents of the interphase transformed are balanced although it presents a small steady state error ($i_T = 160$ mA). The output voltage is regulated at $u_{dc} = 380$ V and the output current is $i_{dc} = 15.79$ A.

Temperatures were measured using a FLIR A655sc Infrared Camera as seen in Figure 22. The final temperatures at the magnetic components were closely matched with the ones given in Table V for nominal operation. Predicted temperature rise errors were lower than 15,0%.



Fig. 20. Experimental results of the 6-kW 5-level CSR prototype: u_{dc} (50.0 V/div and -200.0 V offset), i_{dc} (5.0 A/div), u_a (50.0 V/div) and i_a (10.0 A/div).



Fig. 21. Waveforms of the 6-kW 5-level CSR prototype: $i_{p,1}$ (2.0 A/div and -4.0 A offset), $i_{p,2}$ (2.0 A/div and -4.0 A offset), u_{dc} (200.0 V/div and -400.0 V offset) and u_a (200.0 V/div and -400.0 V offset).



Fig. 22. Temperature measurement using a FLIR A655sc Infrared Camera. Ambient temperature was 40°C. Maximum temperatures: input inductors was 80.2° C; interphase transformers was 67.5° C; output inductor was 93.7° C.

X. CONCLUSIONS

An optimization procedure for the design of the magnetic components for a 5-level CSR was presented based on practical design requirements and aiming at the minimization of the overall converter volume. To the knowledge of the authors the proposed procedure is critical in real applications for this type of converter and has not been previously performed. CSRs are typically seen as having worst performance than voltage source converters and the optimum design of their components is critical if this is to change for any given application.

The input filter inductors, interphase transformers and output inductor were considered and a design based on

a system perspective was proposed. The optimization methodology was exemplarily performed for two switching frequencies, namely 45 kHz and 150 kHz.

The magnetic optimization is initialized with the specification of the inductance values, which is based on the worst operating case, EMC, losses and thermal constraints. The required geometric and magnetic parameters were defined, where it was shown that the Steinmetz coefficients are advantageously updated for the different values of induction and frequency since these change at each switching period of the 5-level CSR. The Steinmetz coefficients dependence on temperature is negligible for the design cases with iron powder cores because the optimal design tends to lead to a temperature close to the defined maximum. However, the behavior was quite different for the ferrite cores used in the interphase transformer and the coefficients were updated accordingly. Dowell's equation was used to compute the copper losses since no air gap is used in the considered components. The iGSE method was used to calculate the core losses with the Steinmetz coefficients updated for each switching period.

The optimization results show that a 150 kHz switching frequency is justified since it results in a reasonably smaller overall converter volume. The experimental prototype reached an efficiency at 6.0 kW of 97.0% and a power density of approximately 22.26 kW/dm³ when considering only the magnetics volume. A 1.1 kW/dm³ overal power density, considering the converter boxed volume, was achieved. Such values are close to the ones reported for voltage source rectifiers employing conventional components and cooling methods.

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BIOGRAPHIES

Lisandra Kittel Ries received the B.S., M.S and Ph.D. degrees in electrical engineering from the Federal University of Santa Catarina, Florianópolis, Brazil, in 2010 and 2013, respectively. She also received the B.S. and M.S. degrees in electrical engineering from the Institut National Polytechnique of Toulouse, Toulouse, France, in 2010. She is currently an Adjunct Professor with the Electrotechnical Department, Federal Institute of Education, Science and Technology of Santa Catarina, Florianópolis, Brazil.

Marcelo Lobo Heldwein (S'99-M'08-SM'13) received the B.S. and M.S. degrees in electrical engineering from the Federal University of Santa Catarina (UFSC), Florianópolis, Brazil, in 1997 and 1999, respectively, and his Ph.D. degree from the Swiss Federal Institute of Technology (ETH Zurich), Zurich, Switzerland, in 2007.

He is currently an Associate Professor with the Department of Electronics and Electrical Engineering at the UFSC. From 1999 to 2003, he worked with industry, including R&D activities at the Power Electronics Institute, Brazil and Emerson Network Power, Brazil and Sweden. He was a Postdoctoral Fellow at the ETH Zurich and at the UFSC from 2007 to 2009. Dr. Heldwein is a member of the Brazilian Power Electronic Society (SOBRAEP). He is also a member of the Advisory Board of PCIM Europe and an Associated Editor of the IET The Journal of Engineering.

His research interests include Power Electronics, Advanced Power Distribution Technologies and Electromagnetic Compatibility.