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OSV-MPC for Harmonic and Zero-Sequence Compensation in Four-Wire Off-Grid Microgeneration Systems Based on SEIG

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ABSTRACT This paper proposes a finite control set model predictive control (FCS-MPC) strategy to address voltage regulation in isolated four-wire microgeneration systems based on self-excited induction generators. The FCS-MPC approach enables a simpler and more functional imbalance controller design. The controller determines the optimal switching vector through a cost function that employs a weighting factor for the different control variables. It effectively tracks distorted current references and prevents voltage imbalance, even in a system without frequency regulation. The employed inverter is a four-leg voltage source inverter, also known as a distribution static synchronous compensator, which provides the necessary reactive power and compensates for harmonic content along with the zero-sequence component, ensuring balanced three-phase voltages at the point of common coupling, even with unbalanced loads. The proposed control leverages the instantaneous power theory for synchronization, thereby replacing methods such as the phase-locked loop. Experimental results validate the effectiveness of the proposed control approach and assess the performance of the system in accordance with power quality standards.

KEYWORDS Off-Grid Microgeneration, Induction Generator, Four-Wire System, Model Predictive Control, Zero-Sequence Voltage, Harmonic Current Compensation.

I. INTRODUCTION

The integration of renewable energy sources into distributed generation systems, particularly in microgeneration systems, offers a promising avenue for reducing costs associated with conventional transmission and distribution infrastructure. Self-Excited Induction Generators (SEIGs) emerge as key components in these systems, drawing from water, wind, or biomass resources [1]–[3]. In this sense, SEIGs play a crucial role in voltage regulation, dependent on the balance between reactive and active power within the AC bus. Nevertheless, they are susceptible to load variations, leading to changes in machine slip and consequent variations in reactive power and synchronous speed. To ensure voltage regulation at the point of common coupling (PCC), various topologies for reactive power compensation have been proposed for SEIGs [4], [5].

Among these solutions, the four-leg inverter, also known in this application as a Distribution Static Synchronous Compensator (DSTATCOM), stands out for its effectiveness in voltage regulation and compensation of reactive power and harmonic components. This type of inverter is adept at providing balanced voltages for a wide range of load configurations, including linear, nonlinear, balanced, and unbalanced loads, with the midpoint of the fourth leg serving as the neutral point for connected loads [6]–[8]. The system studied in this paper is represented in Figure 1. The Model Predictive Control (MPC) is considered an algorithm that utilizes the system model to predict its future behavior and selects the most suitable control action based on an optimization criterion [9]. The predictive controller is widely applied in four-leg converters due to its fast dynamic response, surpassing classical controllers, both proportional-integral (PI) and proportional-resonant (PR) controllers [10]–[16]. The main advantages of using MPC are its ability to track alternating signals, regardless of the designed frequency, handle saturation, switching frequency, and power losses, as well as allow for the inclusion of constraints directly in the optimization process, which enhances control flexibility and robustness [10], [17].

MPC for power electronics can be categorized into two primary groups: continuous control set (CCS-MPC) and finite control set (FCS-MPC). CCS-MPC boasts a significant advantage in its utilization of a continuous control signal coupled with a pulse-width modulator, ensuring a consistent switching frequency. This design simplifies the development of power filters [18]. However, it comes with a trade-off, reducing the control speed by not ensuring the maximum



FIGURE 1. Diagram of the microgeneration system.

possible control action. Additionally, this method excels when dealing with unconstrained optimization problems, allowing for offline analytical solutions. Nevertheless, it can become mathematically complex, particularly when introducing constraints. On the other hand, among the various approaches available, FCS-MPC leverages the inherent discrete characteristics of power converters [9]. Because these converters possess a finite number of switching states, the optimization challenge revolves around predicting system behavior within those available switching states over a shortterm horizon. In this paper, the focus is on the proposed optimal switching vector MPC (OSV-MPC) approach. The OSV-MPC, a subtype of FCS-MPC, is the most popular MPC strategy for power electronics, often referred to as FCS-MPC in the literature [17]. A limitation of OSV-MPC is that, in the absence of a modulator, the switching frequency is not enforced to remain constant. OSV-MPC considers only the possible switching states as the control set, applying one of them during the entire sampling period, simplifying the optimization problem to an enumerated search algorithm, resulting in a simpler and more intuitive solution [10], [19].

This paper proposes the use of OSV-MPC for inverter currents and also for the neutral point voltage at the PCC, which is used to compensate for imbalances caused by unbalanced loads connected to the system. To meet the requirements of voltage regulation, harmonic compensation, and imbalances in the proposed four-wire system, the use of OSV-MPC is essential, with its fast dynamic response, intuitive formulation, and easy consideration of nonlinearities and constraints in the control of variables of different scales, such as current and voltage, in a single control loop. Some challenges of OSV-MPC are encountered in the design of the cost function, but these can be addressed by including a weighting factor (λ) , which aids in the operational performance of the controlled variables [10]. This strategy was not found in the literature for this isolated four-wire microgeneration system based on SEIG and VSI, providing a solution to the control tracking problems indicated in [6], [12], [16].

Experimental results are presented to validate the performance of the proposed control strategy for the system in terms of power quality, according to the IEEE 519-2014 standard for total harmonic distortion (THD) and the IEEE 1159-2009 standard for voltage imbalances. The main contributions of this paper can be summarized as follows:

- Application of OSV-MPC in a four-leg VSI connected to an isolated microgeneration system based on SEIG
- Proposal and implementation of a specific cost function in OSV-MPC strategy that considers voltage imbalance regulation, harmonic tracking, and zero sequence compensation for the off-grid microgeneration system
- The use of instantaneous power theory for system synchronization, based on three-phase voltages, reducing computational burden, enabling an increase in the sampling frequency, differing from [16]

II. SYSTEM DESCRIPTION AND MODELING

From the system diagram (see Figure 1), the inverter is connected to PCC. Each of the sixteen switching states of the four-leg VSI corresponds to one output voltage vector in abc coordinates. The Clarke power invariant transformation can represent these output voltage vectors in the $\alpha\beta0$ frame. The dynamics of the output currents in $\alpha\beta0$ can be written as a discrete-time state-space model for a sampling period T_s , and a forward Euler approximation can be used to predict the future value of the variables by:

$$\hat{\boldsymbol{i}}_{\alpha\beta0,k+1} = (\boldsymbol{I} + \boldsymbol{A}T_{\rm s})\,\boldsymbol{i}_{\alpha\beta0,k} + \boldsymbol{B}T_{\rm s}\left(\boldsymbol{u}_{\alpha\beta0,k} - \boldsymbol{v}_{\alpha\beta0,k}\right),\qquad(1)$$

$$\hat{v}_{0,k+1} = v_{0,k} - \frac{T_{\rm s}}{C_{\rm f}} i_{0,1,k} + \frac{T_{\rm s}}{C_{\rm f}} i_{0,k},\tag{2}$$

where $v_{\alpha\beta0} = [v_{\alpha} \ v_{\beta} \ v_{0}]^{\top}$ are PCC voltages, and $u_{\alpha\beta0}$ are the inverter output voltages. The state-space matrices follow the well-known model presented in [16]:

$$\dot{\boldsymbol{i}}_{\alpha\beta0}(t) = \boldsymbol{A}\boldsymbol{i}_{\alpha\beta0}(t) + \boldsymbol{B}\left(\boldsymbol{u}_{\alpha\beta0}(t) - \boldsymbol{v}_{\alpha\beta0}(t)\right), \quad (3)$$

where

$$\boldsymbol{A} = \begin{bmatrix} \frac{-R_{\rm f}}{L_{\rm f}} & 0 & 0\\ 0 & \frac{-R_{\rm f}}{L_{\rm f}} & 0\\ 0 & 0 & \frac{-R_{\rm f}}{L_{\rm f}} \end{bmatrix} \boldsymbol{B} = \begin{bmatrix} \frac{1}{L_{\rm f}} & 0 & 0\\ 0 & \frac{1}{L_{\rm f}} & 0\\ 0 & 0 & \frac{1}{4L_{\rm f}} \end{bmatrix}.$$
(4)

Moreover, a step ahead can be included to compensate for the implementation time of the microprocessor, according to [20]. In addition, as the frequency of $v_{\alpha\beta0}$ and $i_{\alpha\beta0}$ are much smaller than the sampling frequency, it can be assumed that $v^*_{\alpha\beta0,k} \approx v^*_{\alpha\beta0,k+1}$ and $i^*_{\alpha\beta0,k} \approx i^*_{\alpha\beta0,k+1}$. Table 1 shows the sixteen switching states of the power

Table 1 shows the sixteen switching states of the power converter. Each state corresponds to one output voltage vector represented in the $\alpha\beta0$ frame.

III. CONTROLLERS

The applied control strategy is depicted in Figure 2. The proposed scheme presents an OSV-MPC combined with a linear Proportional-Integral (PI) control. The system inputs include phase voltages, inverter currents, load currents, and the DC bus voltage. Clarke transformation is applied to the voltages and currents to decouple system variables, aiding in the analysis of harmonic and zero sequence components.

Two PI controllers are used: one for controlling the DC bus voltage and the other for the AC voltages at the PCC. The OSV-MPC employs predictive modeling to calculate the optimal switching vector, minimizing a cost function

TABLE 1. Switching States and Output Voltages

| Index | Switching state | | | ate | Output voltage | | |
|-------|-----------------|-------|-------|-------|----------------------------------|-----------------------|------------------------|
| j | S_1 | S_2 | S_3 | S_4 | uα | u _β | u_0 |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 2 | 0 | 0 | 0 | 1 | 0 | 0 | $-\sqrt{3}u_{\rm C}$ |
| 3 | 0 | 0 | 1 | 0 | $-\sqrt{\frac{2}{3}}u_{\rm C}/2$ | $-u_{\rm C}/\sqrt{2}$ | $u_{\rm C}/\sqrt{3}$ |
| 4 | 0 | 0 | 1 | 1 | $-\sqrt{\frac{2}{3}}u_{\rm C}/2$ | $-u_{\rm C}/\sqrt{2}$ | $-2u_{\rm C}/\sqrt{3}$ |
| 5 | 0 | 1 | 0 | 0 | $-\sqrt{\frac{2}{3}}u_{\rm C}/2$ | $u_{\rm C}/\sqrt{2}$ | $u_{\rm C}/\sqrt{3}$ |
| 6 | 0 | 1 | 0 | 1 | $-\sqrt{\frac{2}{3}}u_{\rm C}/2$ | $u_{\rm C}/\sqrt{2}$ | $-2u_{\rm C}/\sqrt{3}$ |
| 7 | 0 | 1 | 1 | 0 | $-\sqrt{\frac{2}{3}}u_{\rm C}$ | 0 | $2u_{\rm C}/\sqrt{3}$ |
| 8 | 0 | 1 | 1 | 1 | $-\sqrt{\frac{2}{3}}u_{\rm C}$ | 0 | $-u_{\rm C}/\sqrt{3}$ |
| 9 | 1 | 0 | 0 | 0 | $\sqrt{\frac{2}{3}}u_{\rm C}$ | 0 | $u_{\rm C}/\sqrt{3}$ |
| 10 | 1 | 0 | 0 | 1 | $\sqrt{\frac{2}{3}}u_{\rm C}$ | 0 | $-2u_{\rm C}/\sqrt{3}$ |
| 11 | 1 | 0 | 1 | 0 | $\sqrt{\frac{2}{3}}u_{\rm C}/2$ | $-u_{\rm C}/\sqrt{2}$ | $2u_{\rm C}/\sqrt{3}$ |
| 12 | 1 | 0 | 1 | 1 | $\sqrt{\frac{2}{3}}u_{\rm C}/2$ | $-u_{\rm C}/\sqrt{2}$ | $-u_{\rm C}/\sqrt{3}$ |
| 13 | 1 | 1 | 0 | 0 | $\sqrt{\frac{2}{3}}u_{\rm C}/2$ | $u_{\rm C}/\sqrt{2}$ | $2u_{\rm C}/\sqrt{3}$ |
| 14 | 1 | 1 | 0 | 1 | $\sqrt{\frac{2}{3}}u_{\rm C}/2$ | $u_{\rm C}/\sqrt{2}$ | $-u_{\rm C}/\sqrt{3}$ |
| 15 | 1 | 1 | 1 | 0 | 0 | 0 | $\sqrt{3}u_{\rm C}$ |
| 16 | 1 | 1 | 1 | 1 | 0 | 0 | 0 |

that considers control variables such as inverter currents and the zero-sequence voltage, related to the fourth or neutral wire. Measurements are sampled at each interval T_s , corresponding to the current and voltage data in discrete time, for each sampling instant k. The instantaneous power theory is used for synchronization by relating inverter active (p) and reactive (q) power with the three-phase voltages at the PCC. Finally, the controller generates the switching state as a control action s, which acts directly on the power switches, in this case, the IGBTs.

The reference currents are generated in the stationary $\alpha\beta0$ reference frame. Employing the p-q theory and the power invariant Clarke transformation, these quantities are found based on the control action of the PCC voltage and DC-bus voltage controllers. The other part of the current references is obtained directly by the load currents:

$$\begin{bmatrix} i_{\alpha}^{*} \\ i_{\beta}^{*} \end{bmatrix} = \begin{bmatrix} i_{\alpha,1} \\ i_{\beta,1} \end{bmatrix} + \frac{1}{v_{\alpha}^{2} + v_{\beta}^{2}} \begin{bmatrix} v_{\alpha} & v_{\beta} \\ v_{\beta} & -v_{\alpha} \end{bmatrix} \begin{bmatrix} p^{*} \\ q^{*} \end{bmatrix}.$$
 (5)

The zero-sequence current compensation is performed by measuring the load current. Then the inverter supplies the required current through the neutral conductor [21]. This compensation can be done using the MPC, directly considering the load current $i_{0,1}$. Thus, the control strategy tracks the amount of current to be injected through neutral,



FIGURE 2. Block diagram of the multi-loop control system.

which is drawn by the load. To control the 0 axis, the predictive controller input requires the load current $i_{0,1}$, that is,

$$i_0^* = i_{0,1}.$$
 (6)

The reference currents found by (5) and (6) are evaluated using a saturation boundary, considering the inverter current ratings. These variables, along with other measurements, are utilized in the OSV-MPC technique, presented in the following subsections.

A. Outer control loops

The prediction model is fed by model states $i_{\alpha\beta0}$ and $v_{\alpha\beta0}$. In addition, the outer loop controllers determine the current references related to the regulation of PCC voltages and also to the DC bus voltage. These values are transformed to $\alpha\beta0$ currents for the cost function of the predictive controller, responsible for controlling the currents processed by the inverter, also using the desired compensation of the load currents $i_{\alpha,1}$, $i_{\beta,1}$, and $i_{0,1}$. Finally, these generate the switching state as a control action. If there are imbalances between the load currents an alternating component appears in the current $i_{0,1}$, causing an imbalance in PCC voltages [12], [16], and also generating an alternating component in the voltage v_0 . The control of these voltage loops depends exclusively on the feedback of the system voltages, making it necessary to measure the PCC phase voltages and the DC bus voltage of the DSTATCOM. Thereupon, discretetime proportional-integral controllers can be used to provide control action in the outer loops as

$$\dot{\boldsymbol{x}}_{\mathrm{I},k} = \boldsymbol{e}_{\mathrm{pq},k}$$

$$\boldsymbol{y}_{k}^{*} = \boldsymbol{k}_{\mathrm{I}}\boldsymbol{x}_{\mathrm{I},k} + \boldsymbol{k}_{\mathrm{P}}\boldsymbol{e}_{\mathrm{pq},k},$$
(7)

with

$$\boldsymbol{x}_{\mathrm{I},k+1} = \boldsymbol{x}_{\mathrm{I},k} + T_{\mathrm{s}}\boldsymbol{e}_{\mathrm{pq},k},\tag{8}$$

where $\mathbf{x}_{I,k} = [x_{I,p,k} \ x_{I,q,k}]^{\top}$ are the accumulation variables for the error integration, $\mathbf{y}_{k}^{*} = [p_{k}^{*} \ q_{k}^{*}]^{\top}$, \mathbf{k}_{P} and \mathbf{k}_{I} are respectively the proportional and integral gain of the PI controller, arranged as column vectors as $\mathbf{k}_{\rm P} = [K_{\rm P,p} \ K_{\rm P,q}]^{\top}$ and $\mathbf{k}_{\rm I} = [K_{\rm I,p} \ K_{\rm I,q}]^{\top}$, and $\mathbf{e}_{\rm pq}$ represents the error:

$$\boldsymbol{e}_{\text{pq},k} = \begin{bmatrix} u_{\text{C},k}^* - u_{\text{C},k} & v_{\text{PCC},k}^* - v_{\text{PCC},k} \end{bmatrix}^\top.$$
 (9)

This implementation is particularly suitable for applying the conditional integration anti-windup technique. For the design of the PI controllers, the method described in 16 was adopted, considering an adequate gain margin, and a phase margin of at least 45 degrees to prevent oscillations or instability. The proportional and integral gains are tuned according to the stability criteria to achieve low frequency, avoiding disturbances caused by interaction with the switching frequency and the variables controlled by the MPC.

B. Model predictive control description

MPC aims to predict the future behavior of the variables. Therefore, an appropriate control action must be defined to bring these variables close to the reference values. In this regard, the measured variables x_k are used to compensate for control implementation time through a predictive function f_p that relates the sampled values and the present state of the inverter s_k , for getting \hat{x}_{k+1} . Thus, to predict future values of the system, the predictive function is used to obtain $\hat{x}_{k+2,i}$, which represents the prediction to \hat{x}_{k+2} for the inverter possible states s_i . Subsequently, a cost function f_g can be defined to find the optimal state between the sixteen resulting values, for this, the cost function will relate the references of the variables and their predicted values. Therefore, the control action s_{k+1} that brings x as close as possible to the desired reference x^* should minimize the cost function according to:

$$g_j = f_g \left\{ \mathbf{x}^*, \hat{\mathbf{x}}_{k+2,j} \right\},$$
 (10)

where

$$\hat{\boldsymbol{x}}_{j,k+2} = f_{p}\left\{\hat{\boldsymbol{x}}_{k+2}, \boldsymbol{s}_{j}\right\}, \ \forall j \in \{1, 2, ..., 16\},$$
(11)

where f_p is the discrete model for the predicted variables $i_{\alpha\beta0}$ and v_0 . There are several ways to define a cost function depending on the nature of the different terms involved in the problem formulation [17]. The proposed cost function is defined as:

$$g_{j} = \left(i_{\alpha}^{*} - \hat{i}_{\alpha,k+2,j}\right)^{2} + \left(i_{\beta}^{*} - \hat{i}_{\beta,k+2,j}\right)^{2} + \left(i_{0,1} - \hat{i}_{0,k+2,j}\right)^{2} + \lambda \hat{v}_{0,k+2,j}^{2},$$
(12)

where λ is a proper weight factor and the zero axis voltage reference is set to zero.

Stability analysis in FCS-MPC presents a challenge and is often approached with heuristic methods. Some studies adopt recursive approaches to support this analysis. For instance, [22] applies FCS-MPC with the Lyapunov function for current control in VSI, ensuring stability and convergence of current errors. Similarly, [23], [24] utilize the Lyapunov function to guarantee closed-loop stability.

C. Cost function with weighting factor

One of the main advantages of OSV-MPC is its flexibility in the cost function, allowing the inclusion of additional terms that may represent a prediction for another system variable, a constraint, or a requirement of the system. This flexibility enables the achievement of various control objectives, such as improving system performance and power quality [25].

This approach acknowledges that different variables in a cost function may have distinct physical natures (current, voltage, reactive power, switching losses, torque, flux, etc.), which can lead to challenges such as variable coupling or overestimation of the importance of one term compared to others. To address this, weighting coefficients or factors (λ), are commonly used, aiding in balancing each variable in the cost function [19]. Depending on the nature of the terms in the cost function, they can be grouped into distinct categories, which facilitates the selection of weighting factors.

Higher values of these factors increase the priority assigned to the corresponding term, causing switching penalties on the tracking error [26], [27]. Properly adjusting the weighting factors is crucial in the design of an OSV-MPCbased controller, as different operational conditions require different priorities. Currently, the weighting factors are determined empirically, as there is no analytical or numerical method for obtaining an optimal solution for these coefficients in the literature [25]. Therefore, a practical approach is to select an operating condition and implement different weighting factors, then analyze the resulting behaviors to determine their most appropriate performance

D. MPC algorithm

The MPC Algorithm 1 is shown below in pseudocode form. To solve the optimization problem, it uses a *for loop* that solves the prediction equations for each voltage vector.

| Algorithm 1: Proposed MPC algorithm | | | | |
|--|--|--|--|--|
| Sampling $i_{\alpha\beta0,k}, v_{\alpha\beta0,k}, i_{\alpha\beta0,1,k}, u_{C,k};$ | | | | |
| $\hat{\boldsymbol{i}}_{\alpha\beta0,k+1}, \hat{\boldsymbol{v}}_{0,k+1}, \leftarrow f_{\mathrm{p}}\left\{\boldsymbol{x}_{k}, \boldsymbol{s}_{k}\right\};$ | | | | |
| $g_{\min} \leftarrow \infty;$ | | | | |
| for each j from 1 to 16 do | | | | |
| $\hat{\boldsymbol{i}}_{\alpha\beta0,k+2,j} \leftarrow f_{\mathrm{p}} \left\{ \hat{\boldsymbol{i}}_{\alpha\beta0,k+1}, \boldsymbol{s}_{j}, \boldsymbol{v}_{\alpha\beta0,k} \right\};$ | | | | |
| $\hat{v}_{0,k+2,j} \leftarrow f_{p} \left\{ \hat{v}_{0,k+1}, \hat{i}_{0,k+1}, i_{0,l,k} \right\};$ | | | | |
| $g_j = f_g \left\{ x_{k+2}^*, \hat{x}_{k+2,j} \right\};$ | | | | |
| if $g_j < j_{\min}$ then | | | | |
| $j_{\min} \leftarrow g_j;$ | | | | |
| $j_{\text{opt}} \leftarrow j;$ | | | | |
| end | | | | |
| end | | | | |
| $s_{k+1} \leftarrow s_{j_{\text{opt}}};$ | | | | |

The algorithm starts by sampling the measurement of the system states and predicting currents and voltages for the instant k + 1, using the prediction function f_p , which relies on the current states x_k and the applied switching state

 s_k . This enables the prediction of $\hat{i}_{\alpha\beta0,k+1}$ and $\hat{v}_{0,k+1}$, and compensates for the delay caused by the implementation in the microprocessor, while the next control action is being calculated. The cost function g_{\min} , initialized with a very high value, is designed to be minimized by comparing the reference values with the predicted responses. The algorithm then iterates over 16 combinations of switching states. For each voltage vector, the variables $\hat{i}_{\alpha\beta0,k+2,j}$ and $\hat{v}_{0,k+2}$ are predicted. The optimal vector is selected by determining which g_j is lower than the current g_{\min} , updating g_{\min} and storing the index j_{opt} . Finally, the optimal vector j_{opt} is directly applied to control the power switches.

IV. EXPERIMENTAL RESULTS

In this section, the validation of the proposed OSV-MPC design is presented through experimental results obtained from the test bench illustrated in Figure 3.



FIGURE 3. Experimental setup.

The results are based on the SEIG and VSI parameters described in Table 2. The first test investigates the effect of selecting an appropriate weighting factor value in the cost function, which is used to achieve optimal control performance based on the operational conditions for load connection. The second test involves observing the connection of a 4.2 kVA three-phase nonlinear load to the system. This load is a three-phase rectifier that causes voltage sags and a high harmonic spectrum in the system voltage, adversely affecting performance concerning power quality standards. The third condition involves the connection of three separate 1 kVA single-phase loads, connected respectively to phases a, b, and c, consisting of three single-phase rectifiers. These loads not only cause voltage harmonics but also generate imbalances that reflect on the neutral conductor of the system. The verification of the experimental results analyzes compliance with power quality standards, according to IEEE 519 for voltage THD and IEEE 1159-2009 for voltage imbalance.

The control algorithm is implemented on a TMS320F28335 DSP with a sampling period of $T_s = 25 \ \mu$ s, enhancing control performance compared to the sampling period of $T_s = 40 \ \mu$ s used in the study [8]. The algorithm execution time for each task in the DSP is presented in Table 3.

| TABLE 2. | System | Parameters |
|----------|--------|------------|
|----------|--------|------------|

| Description | Value | |
|------------------------------|---|--|
| SEIG | | |
| Nominal power | $P_{\rm n} = 3.7 \ \rm kW \ (5 \ \rm cv)$ | |
| Line voltage (RMS) | $V_{\rm s} = 380 {\rm V}$ | |
| Nominal rotor speed | n = 1730 rpm | |
| Frequency | f = 60 Hz | |
| Power factor | FP = 0.81 | |
| Excitation capacitor | $C_{\rm f} = 40 \ \mu {\rm F}$ | |
| Thévenin equivalent | $L_{\rm g}$ = 5 mH, $R_{\rm g}$ = 0.2 Ω | |
| Inverter | | |
| Sampling period | $T_{\rm s} = 25 \ \mu {\rm s}$ | |
| DC bus voltage | $u_{\rm C} = 650 \ {\rm V}$ | |
| DC-link capacitor | $C = 4.700 \ \mu F$ | |
| Output filter | $L_{\rm f}$ = 3.2 mH, $R_{\rm f}$ = 0.26 Ω | |
| PI Controllers | Gains | |
| DC bus voltage $u_{\rm C}$ | $K_{\rm P,p} = 40, K_{\rm I,p} = 250$ | |
| PCC voltage v _{PCC} | $K_{\rm P,q} = 5, K_{\rm I,q} = 1000$ | |

TABLE 3. Computational Time

| Task | Total execution time |
|-----------------------------------|----------------------|
| OSV-MPC algorithm from [8] | 27.5 µs |
| OSV-MPC algorithm from this paper | 22.3 µs |

A. Weighting factor selection

For the application and experimental test conditions, it is necessary to define, through an operating point, an appropriate value for the weighting factor within the cost function. Careful balancing is crucial to avoid compromising the stability and performance of the system. To this end, an operating condition was selected to analyze the variation of the weight factor.

Figure 4 shows the voltage harmonic spectrum of the system with the weight factor variation in the presence of a 4.2 kVA three-phase nonlinear load.

The weighting factor with the lowest voltage THD for this operating condition is $\lambda = 0.5$, which is considered throughout the results of this paper due to its better performance.

B. Reference tracking of system voltages

The response of the OSV-MPC control can be assessed by evaluating the performance of the applied controllers under reference signal variations for the variables of interest. These variations allow us to observe the ability of the controllers to converge to imposed changes, demonstrating their adequate response.

Figures 5(a), 5(b), and 5(c) respectively, present the transient response of the voltage control loops, the inverter DC bus, the PCC voltages on v_{PCC} and the PCC voltages on axis 0. In this case, there is a variation in the voltage



FIGURE 4. Comparison of voltage THD for different weighting factors, when connecting a 4.2 kVA three-phase nonlinear balanced load.

reference signal $u_{\rm C}^*$ from 650 V to 600 V, as well as in v_{PCC}^* from 311 V to 261 V. Finally, for v_0^* , the control of v_0 is initially inhibited, and then there is a variation in the voltage reference signal v_0^* from 0 V to 30 V, compensating for variations on axis 0 from no load to compensation on.



FIGURE 5. Experimental results for reference steps on the controlled voltages: (a) Dc bus. (b) PCC voltage. (c) PCC voltage on axis 0 from no-load to compensation on.

C. Analysis for three-phase nonlinear load

The connection of a three-phase nonlinear load to the microgeneration system introduces nonlinear characteristics in each phase of the system, with symmetrical magnitude and distribution due to the circuit characteristic. This results in harmonics that distort the voltage waveform. Although this load does not directly cause imbalances among phases, it can still impact compliance with power quality standards. In this context, an uncontrolled three-phase rectifier is connected to the PCC.

Figure 6 shows the harmonic content of the currents from the three-phase nonlinear load, as well as the power drawn by the load at the instant of its connection.



FIGURE 6. Content of the three-phase nonlinear load: (a) Harmonic content of the load current. (b) Power drawn by the load.

Figure 7 shows the reference tracking for the currents i_{α} , i_{β} and $i_{0,1}$ for a 4.2 kVA balanced nonlinear load. In this way, controllers present the ability to quickly trace the references imposed by the load on i_{α}^* , i_{β}^* and $i_{0,1}$. Reference $i_{0,1}$ remains null due to the characteristic of a balanced threephase system without zero-sequence components.



FIGURE 7. Results from DSP data for reference tracking in the $\alpha\beta0$ frame for a 4.2 kVA balanced three-phase nonlinear load.

Figure 8 shows the voltage waveform at the SEIG terminals $(v_a, v_b \text{ and } v_c)$, the PCC voltage (v_{PCC}) , the load current $(i_{a,1}, i_{b,1} \text{ and } i_{c,1})$, the current processed by DSTATCOM $(i_a, i_b \text{ and } i_c)$. In this Figure, an adequate response for the regulation of the microgeneration system can still be observed, even with a nonlinear load.





FIGURE 9. Steady-state behavior under 4.2 kVA three-phase nonlinear load: voltage and current waveforms, before and after compensation.

FIGURE 8. Steady-state behavior for 4.2 kVA load.

In the Figure 9, the waveform of the DC bus voltage of the VSI (u_C) , the phase a voltage at the PCC (v_a) , the phase a current of the SEIG $(i_{a,g})$, and the load current $(i_{a,l})$, were analyzed with and without compensation. It can be observed that with the control method, the microgeneration system provides adequate regulation even with a nonlinear load.

Figure 10 illustrates, from FLUKE 435, the voltage performance under the connection of a 4.2 kVA three-phase nonlinear load. Figure 10(a) shows the voltage sag without control. Figure 10(b) displays the harmonic content of the voltages without the designed control action, exceeding the limits set by the IEEE 519-2014 recommendations. In Figure 10(c), the PCC voltages are shown compensated by the control action performed by the DSTATCOM. Figure 10(d), analyzes the harmonic content of the steady-state voltage at the SEIG terminals. It also presents a reduction due to the compensating currents generated by the DSTATCOM, complying with the IEEE 519-2014 limits of 5% for individual harmonics and 8% for total harmonic distortion (THD).

D. Analysis for single-phase nonlinear loads

In the microgeneration system, connecting single-phase nonlinear loads subjects each phase to different current magnitudes and characteristics. This causes imbalances, generating not only harmonic distortions in the voltage waveforms but



FIGURE 10. PCC voltages for three-phase balanced nonlinear load without compensation (4.2 kVA): (a) Uncompensated three-phase voltage waveforms. (b) Harmonic content without compensation. (c) Compensated three-phase voltage waveforms. (d) Harmonic content with compensation.

also introducing zero-sequence components, which compromise three-phase balance. The resulting unbalanced currents can flow through the neutral conductor, increasing losses and causing undesirable variations in system voltage levels. In this context, three single-phase uncontrolled rectifiers are sequentially connected to the PCC on phases a, b, and c, remaining simultaneously connected. Figure 11 shows the harmonic content of the currents from the three nonlinear single-phase loads, as well as the power drawn by the load, respectively, highlighting the instant when both are simultaneously connected to the system.



FIGURE 11. Content of the 1 kVA single-phase nonlinear loads: (a) Harmonic content of the load current. (b) Power drawn by the load.

Figure 12 shows the reference tracking for currents i_{α} , i_{β} , and i_0 for three 1 kVA single-phase nonlinear loads. These loads are uncontrolled single-phase rectifiers with different capacitor values, causing an imbalance among the system phases. The capacitor values in the 1 kVA single-phase rectifiers are 20 µF in the load connected to phase a, 15 µF in the load connected to phase b, and 100 µF in the load connected to phase c. The designed controllers demonstrate the ability to quickly track the references imposed by the loads on i_{α}^{*} , i_{β}^{*} and $i_{0,1}$. The reference $i_{0,1}$ represents the unbalanced current from the characteristics of a three-phase system with zerosequence components. Even with the connection of three single-phase rectifiers with distinct capacitances, resulting in a high system imbalance, the response is adequate for voltage regulation.



FIGURE 12. Results from DSP data for reference tracking in the $\alpha\beta0$ frame at 1 kVA for three different single-phase loads connected to each phase simultaneously.

Figure 13 shows the transient response of the system to the connection of the nonlinear single-phase loads. It is noted that the inverter currents mitigate the effects of distorted load currents, compensating for disturbances and maintaining voltage regulation at the PCC.



FIGURE 13. Connection transient of three single-phase loads (1 kVA rectifiers) connected to each phase (a, b, and c), respectively.

Figure 14 shows the voltage waveform at the SEIG terminals (v_a , v_b and v_c), The voltage (v_{PCC}), the load current ($i_{a,1}$, $i_{b,1}$ and $i_{c,1}$), the current processed by DSTATCOM (i_a , i_b and i_c). Even with the connection of three single-phase rectifiers with different capacitances, resulting in a high imbalance in the system, the response is adequate for voltage regulation.

Figure 15 shows the DC bus voltage of the VSI ($u_{\rm C}$) and the zero-sequence voltage (v_0) during the connection of three uncontrolled single-phase rectifiers with different capacitances. These single-phase nonlinear loads naturally cause imbalances in the system, resulting in oscillations in the neutral wire voltage v_0 . The quick response of the system to load transients is observed, maintaining zero voltage in the v_0 axis due to the performance of the OSV-MPC.

For Figure 16, the waveforms of the DC bus voltage of the VSI ($u_{\rm C}$), the phase a voltage at the PCC ($v_{\rm a}$), the phase a current of the SEIG ($i_{\rm a,g}$), and current processed by the DSTATCOM in phase c, ($i_{\rm c}$) were analyzed.

It was observed that the microgeneration system provided an adequate response for regulation, even with the connection of three unbalanced single-phase nonlinear loads.

In Figure 17, the compensation of imbalance by the DSTATCOM can be analyzed, resulting in balanced terminal voltages as shown in Figure 17(a). This meets the IEEE 1159-2009 standard for imbalance below 2% as illustrated in Figure 17(b) and also complies with the IEEE 519-2014 standard for voltage harmonics, as shown in Figures 17(c) and 17(d).

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FIGURE 14. Results for three single-phase loads (rectifiers) of 1 kVA connected to each phase (a, b, and c).



FIGURE 15. Transient response to connecting three 1 kVA nonlinear single-phase loads to phases (a, b, and c).

V. CONCLUSION

This paper presented an OSV-MPC approach for voltage regulation at the PCC, along with imbalance regulation, in a four-wire SEIG-based off-grid microgeneration system. It also addressed harmonic and zero-sequence compensation. Experimental results validated the proposed method, demonstrating appropriate transient and steady-state responses. The THD analysis showed that the harmonic content in the PCC



FIGURE 16. Steady-state behavior under single-phase nonlinear loads of 1 kVA connected to each phase (a, b, and c): voltage and current waveforms with compensation.



FIGURE 17. Steady-state behavior of PCC voltages (FLUKE 435) with compensation for three unbalanced single-phase nonlinear loads of 1 kVA: (a) Three-phase voltage waveforms. (b) PCC voltage imbalance. (c) Voltages harmonic content. (d) Harmonic content table.

voltages met the IEEE 519-2014 standard recommendations. Furthermore, the voltage imbalance control drove the variables within the limits suggested by the IEEE 1159-2009 standard, representing a good performance even in a system with unregulated frequency. The design and implementation of the controller are straightforward and also require no hardware modifications. The proposed OSV-MPC strategy contributed to a better performance of the isolated microgeneration system presented.

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AUTHOR'S CONTRIBUTIONS

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